

FIG. 1
PRIOR ART

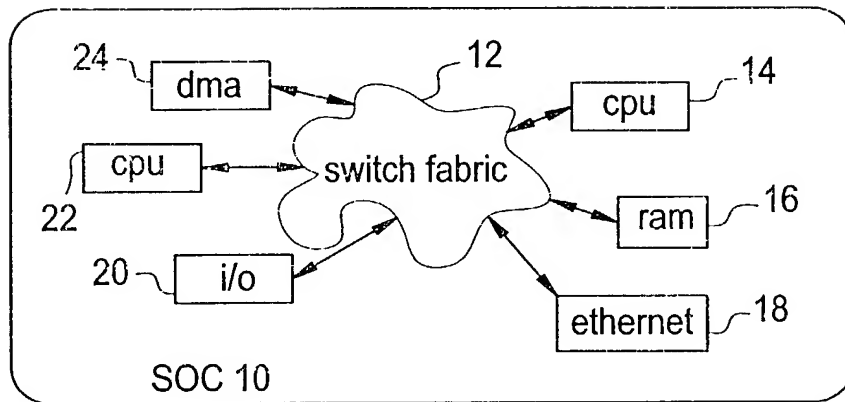
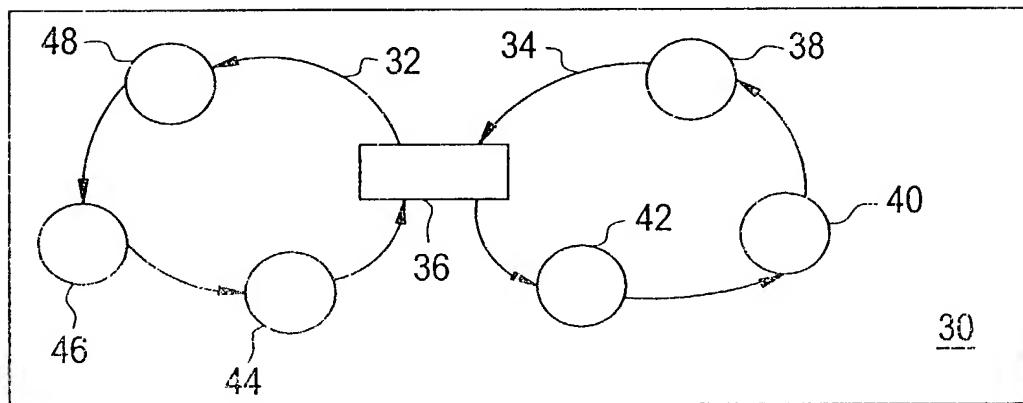


FIG. 2



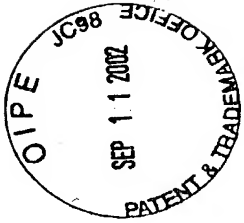


FIG. 3

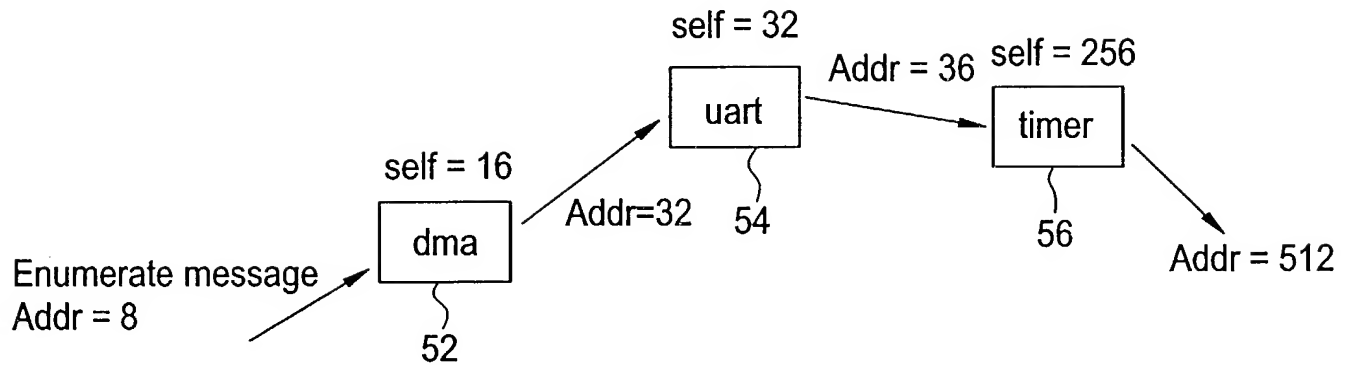


FIG. 4

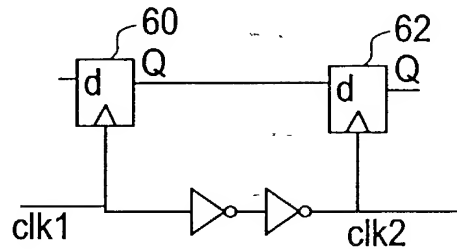
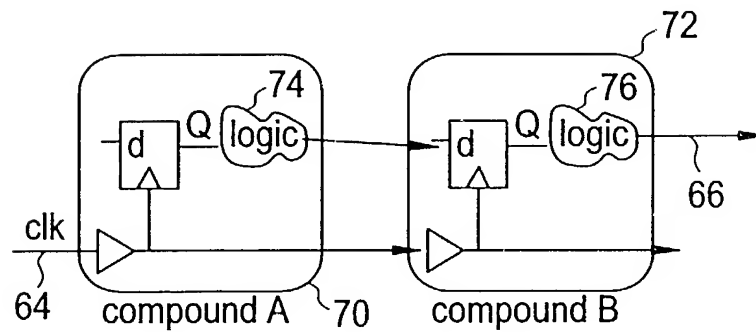


FIG. 5



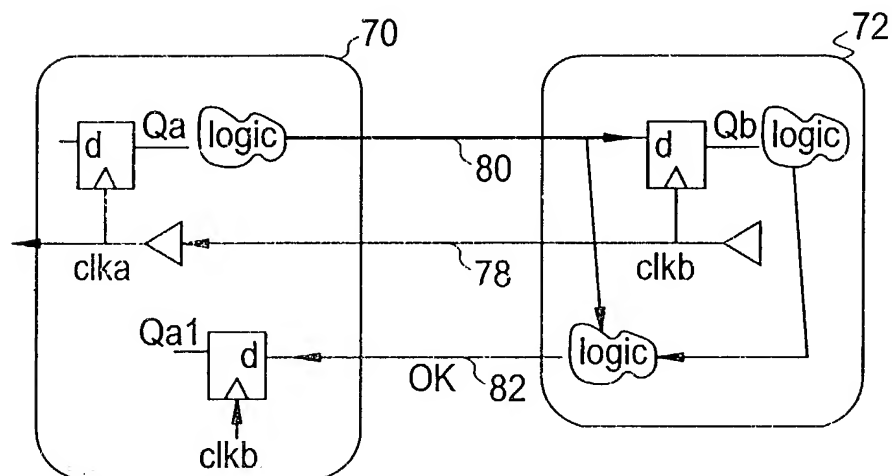


FIG. 8

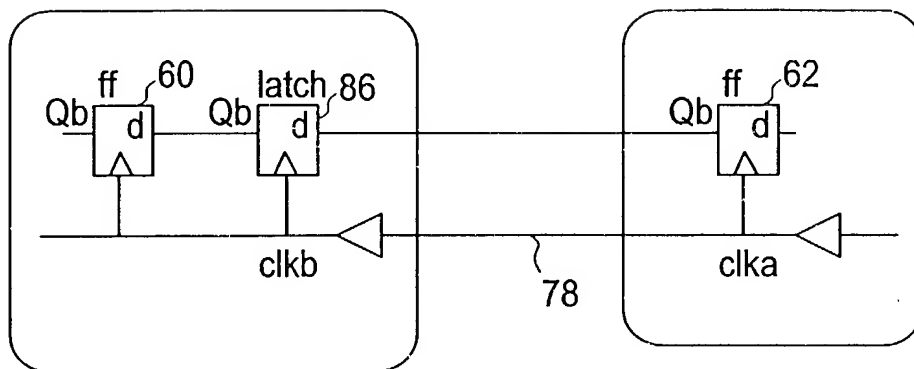


FIG. 9

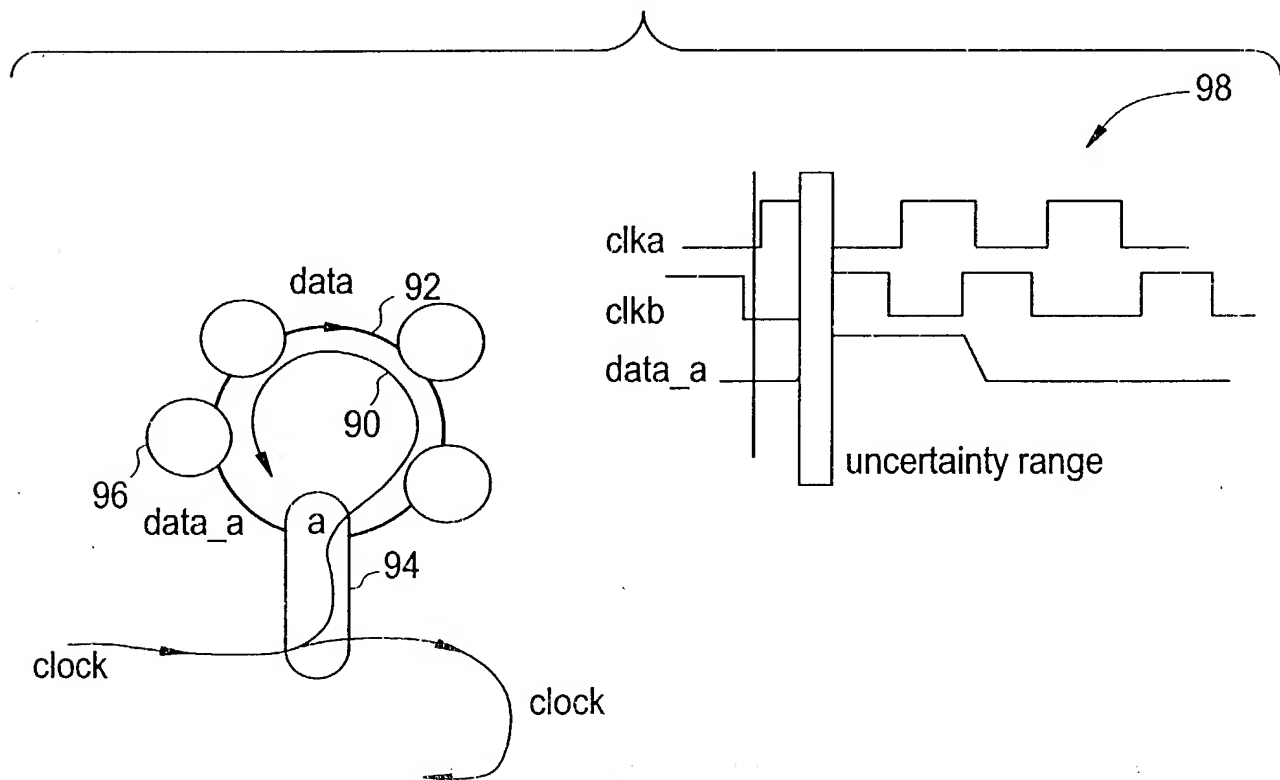


FIG. 10

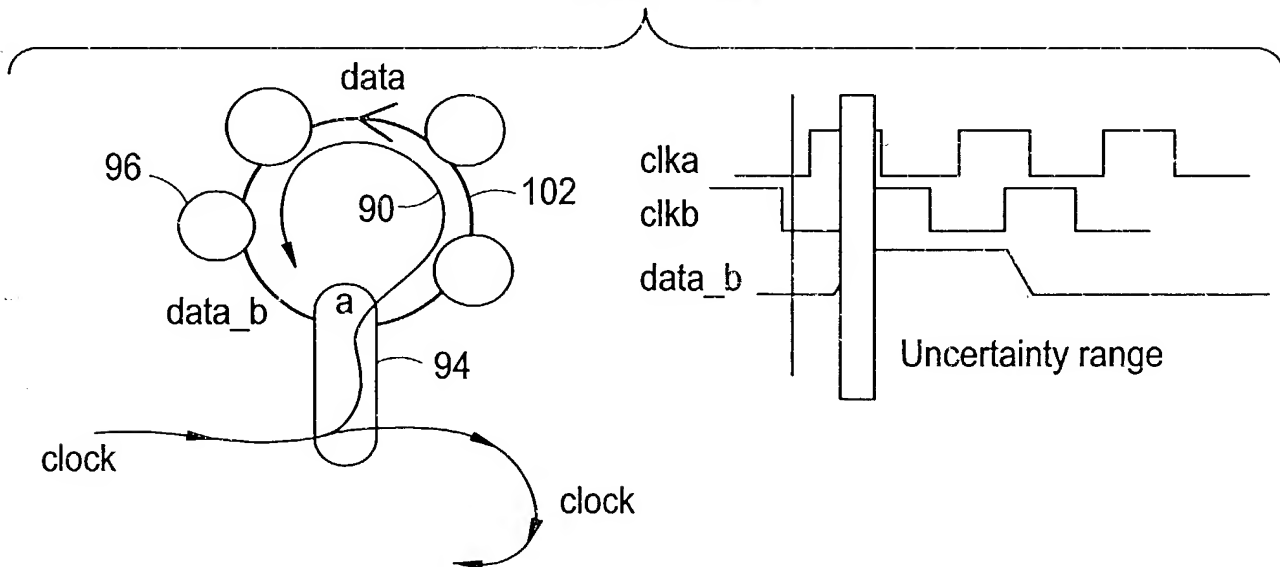


FIG. 11

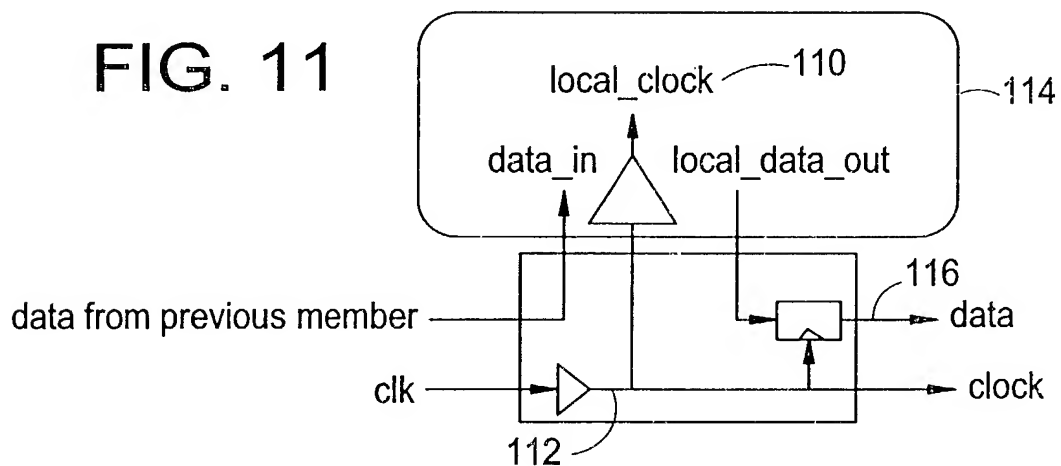


FIG. 12

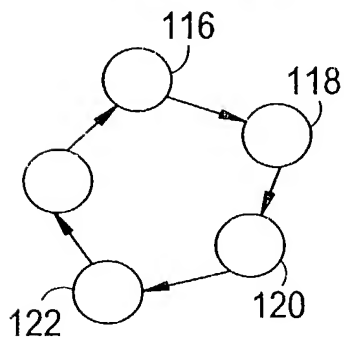
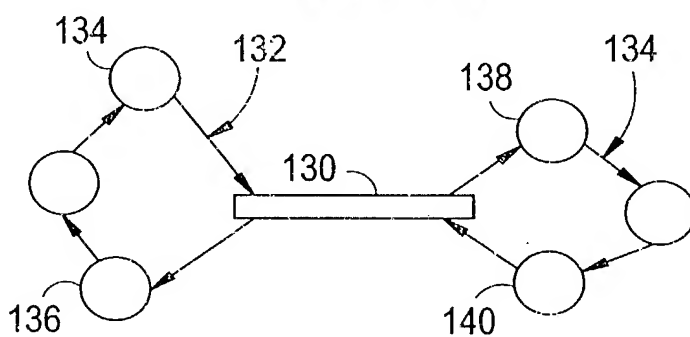


FIG. 13



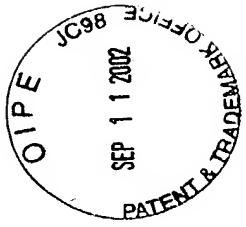


FIG. 14

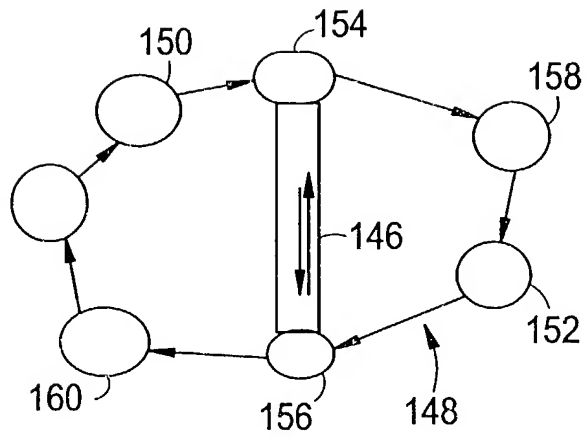


FIG. 15

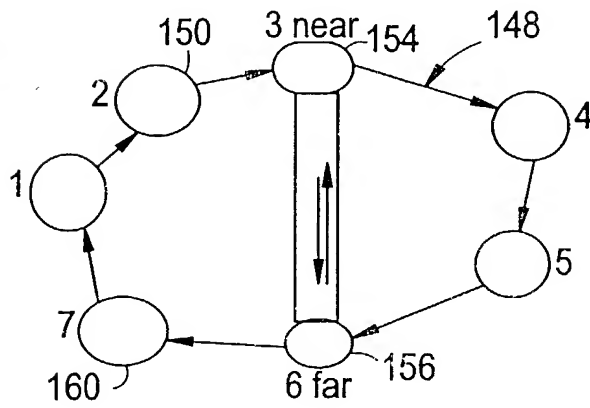
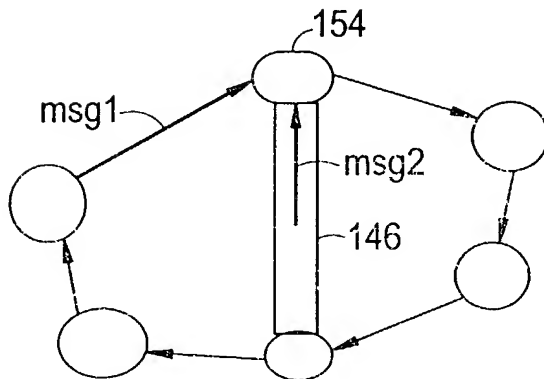
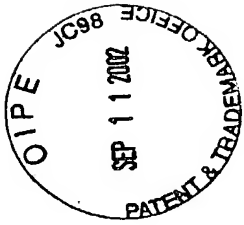


FIG. 16





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FIG. 17

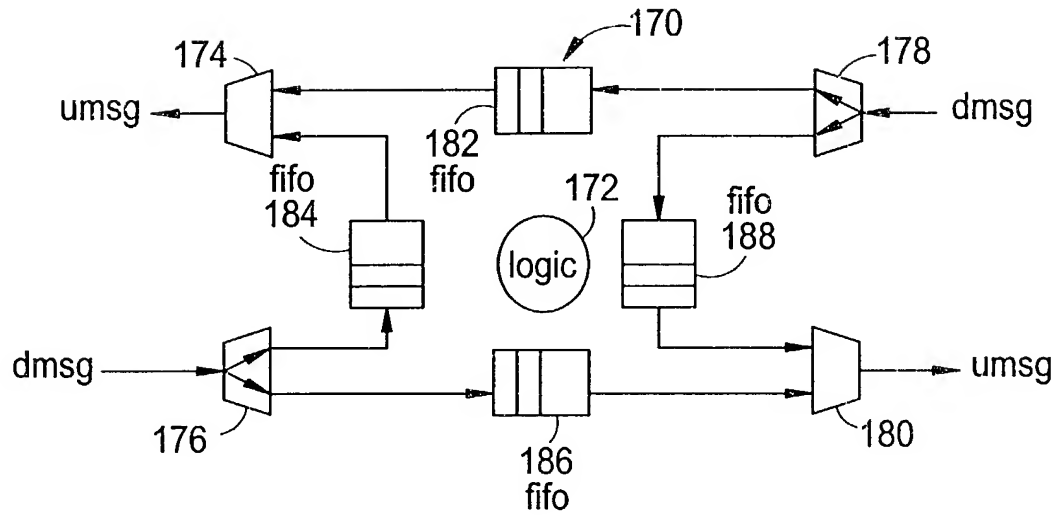


FIG. 18

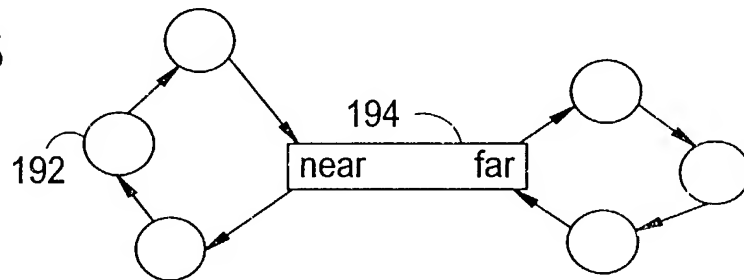
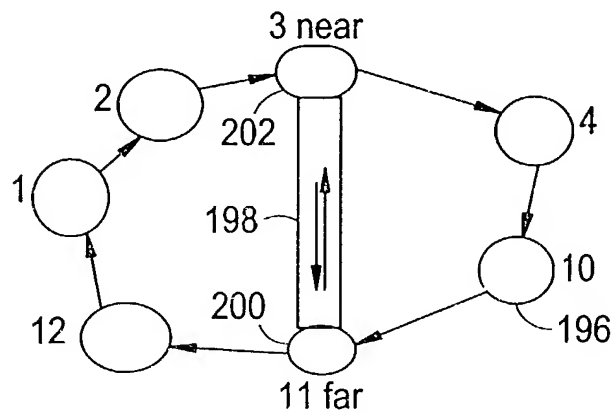
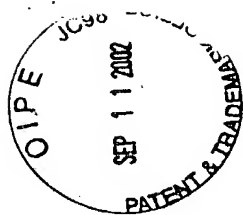


FIG. 19





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FIG. 20

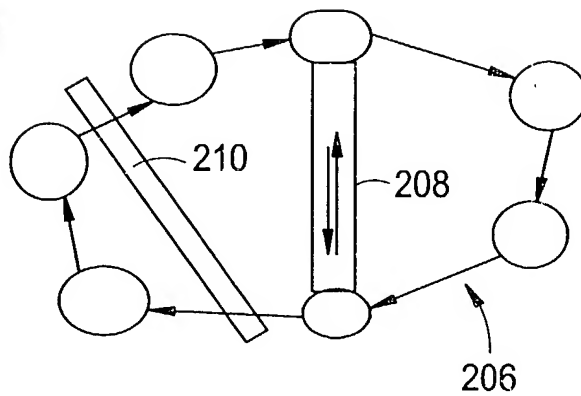


FIG. 21

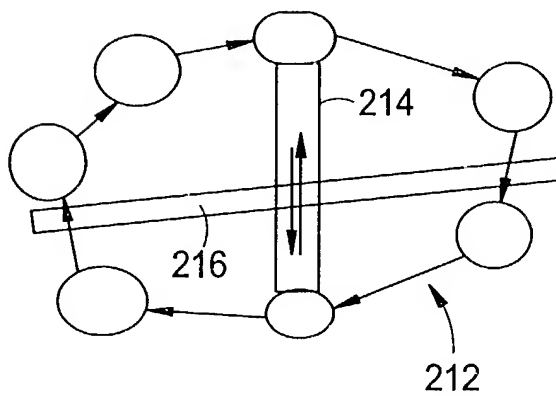
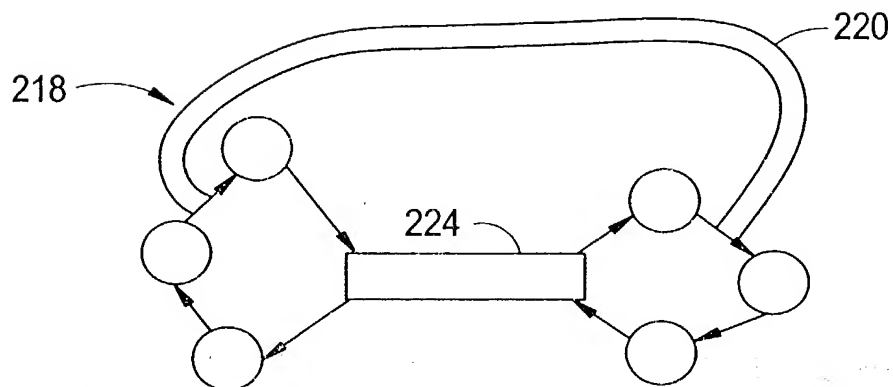
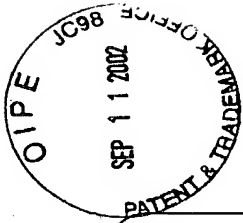


FIG. 22





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FIG. 23

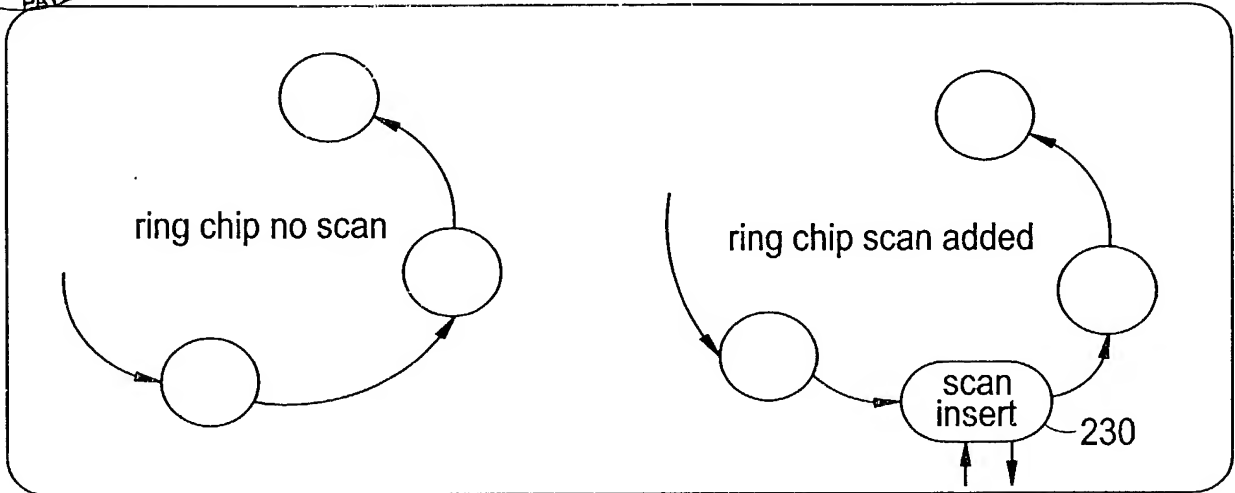


FIG. 24

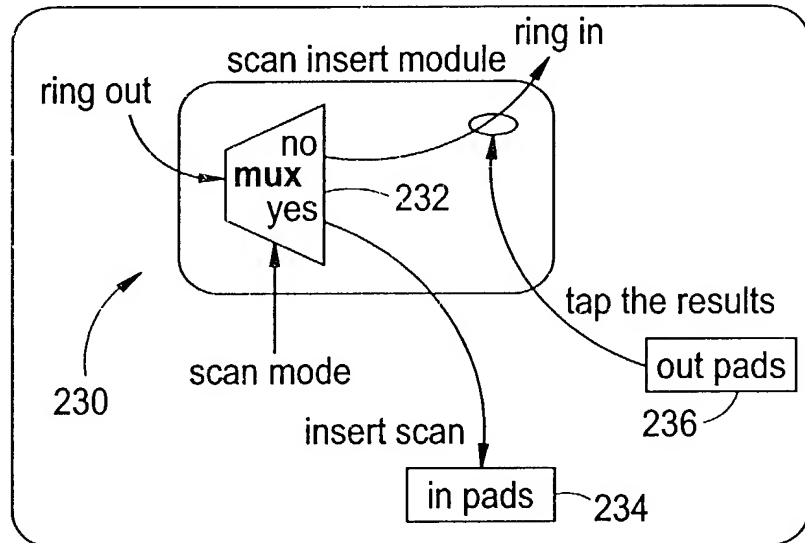
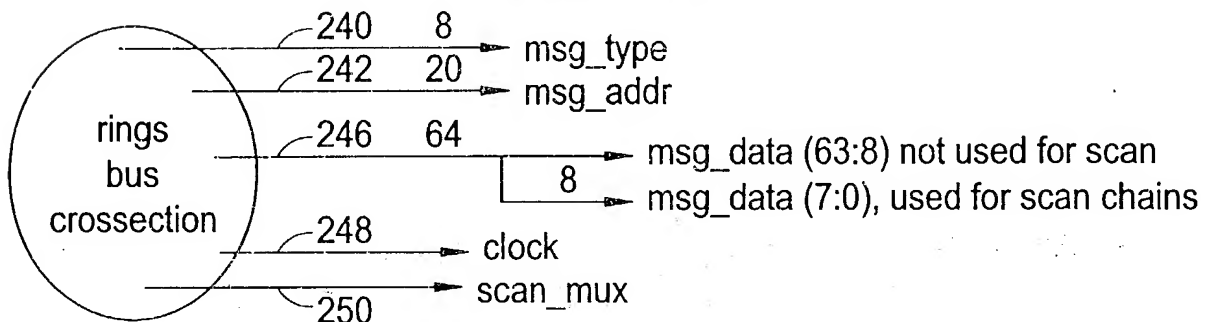


FIG. 25





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FIG. 26

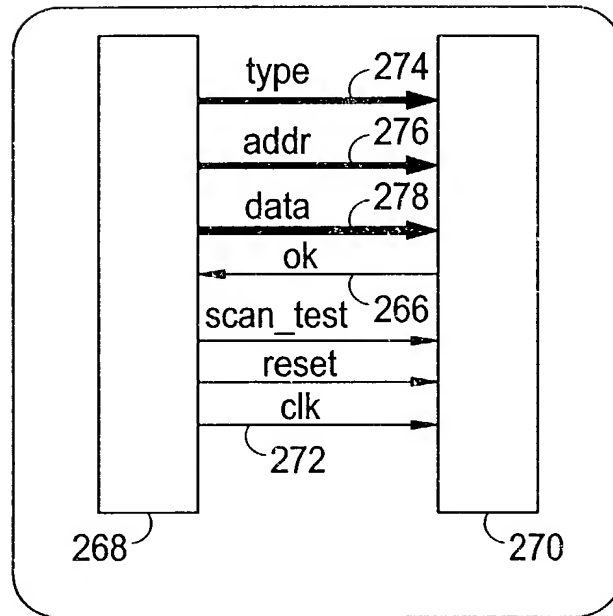


FIG. 27

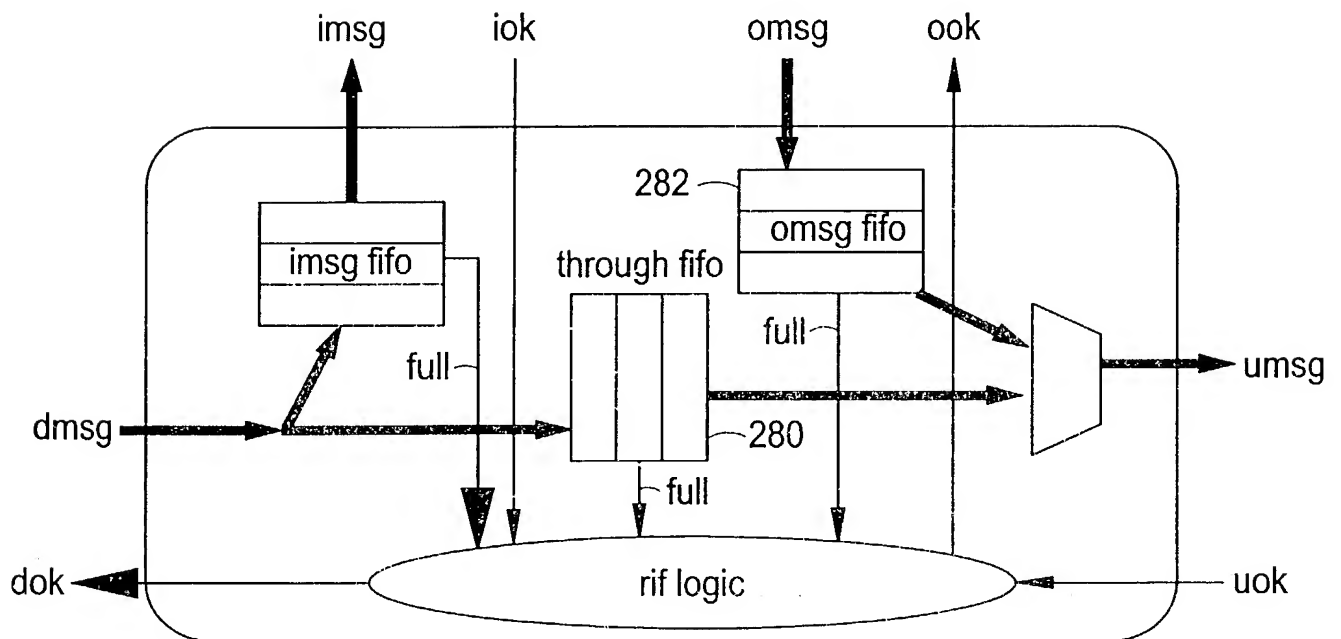
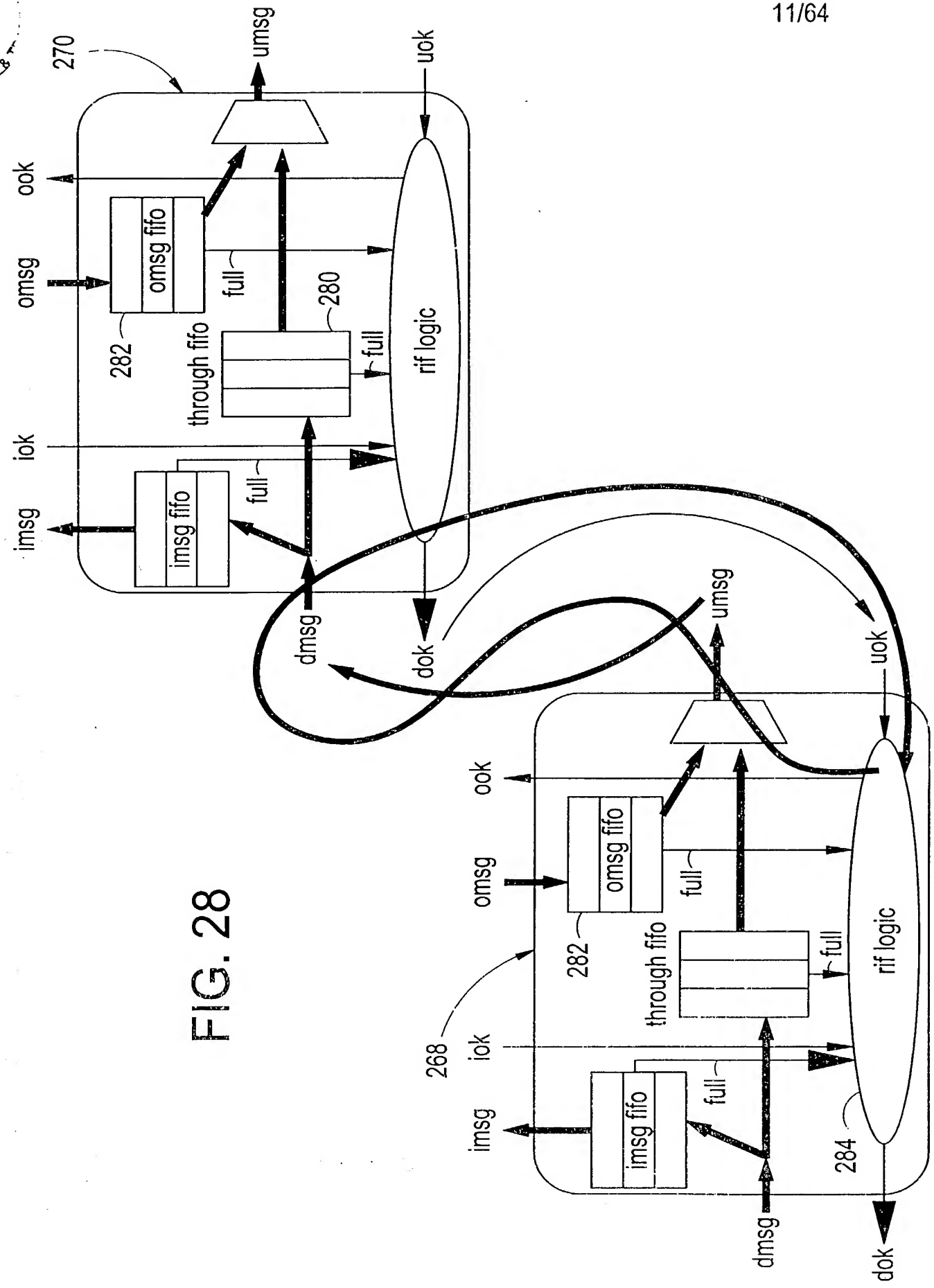
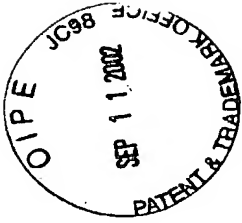


FIG. 28

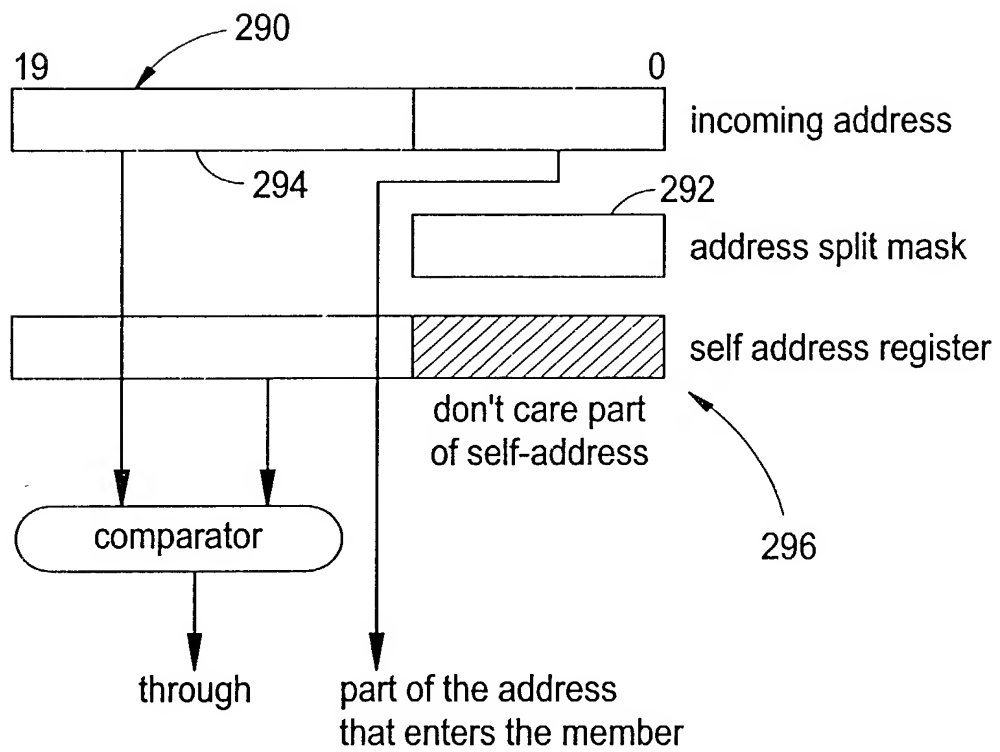


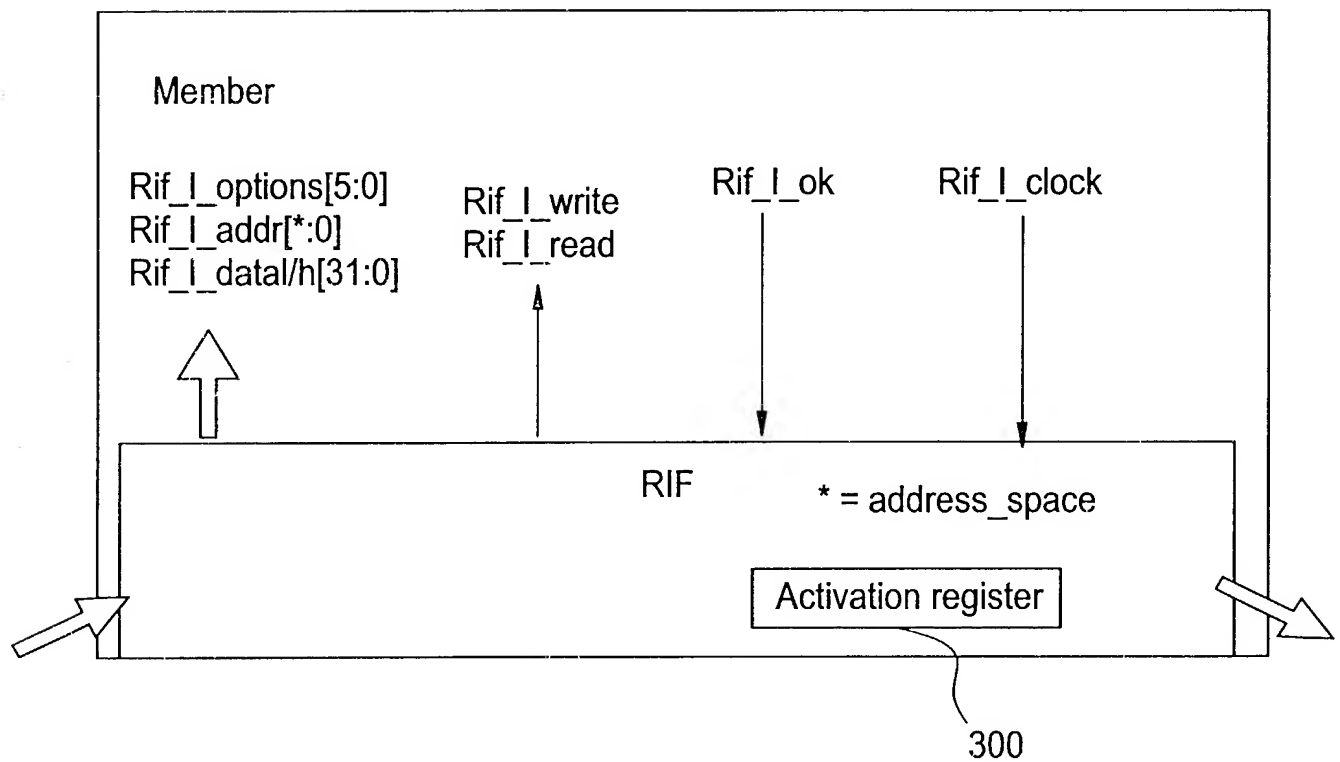
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FIG. 29





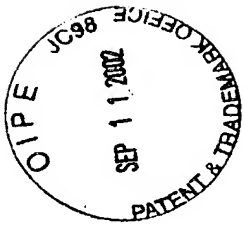
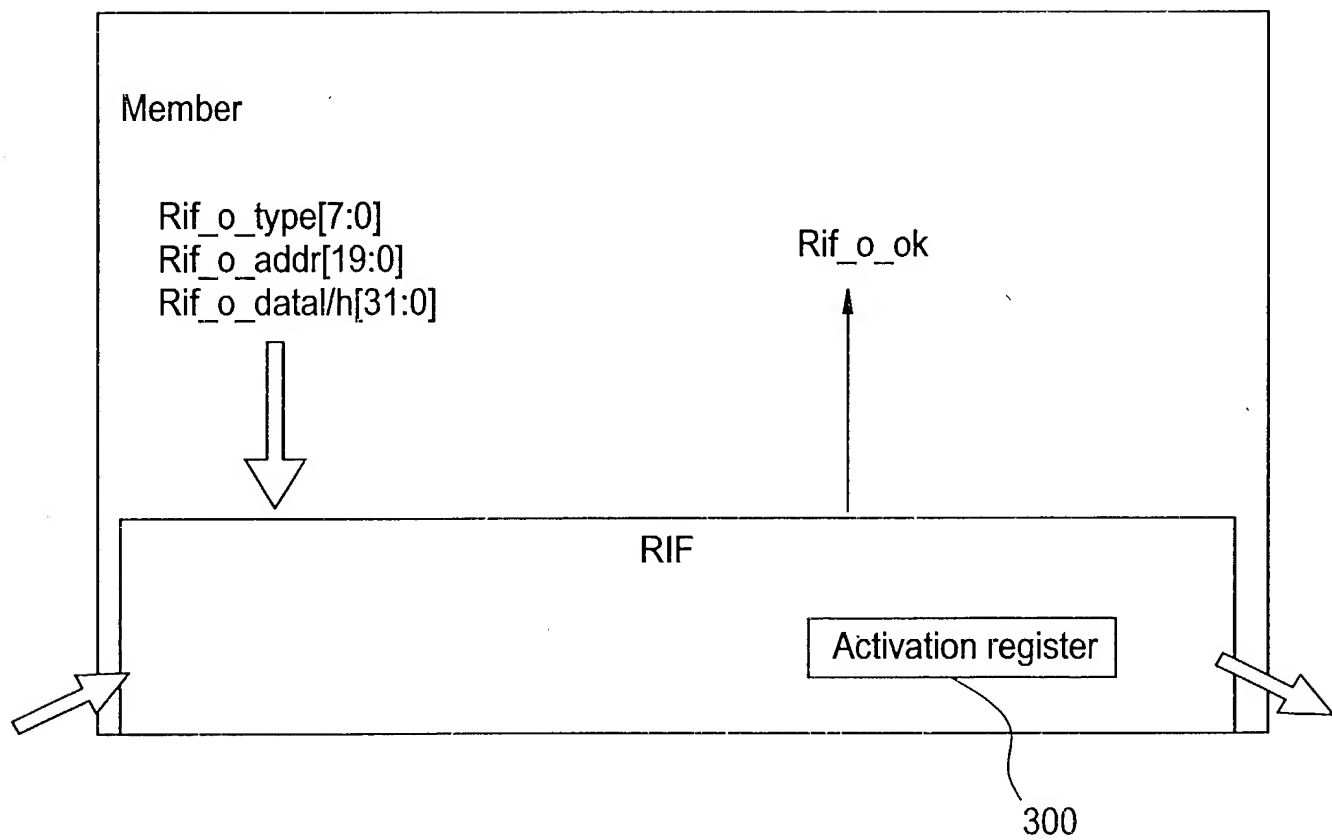


FIG. 32



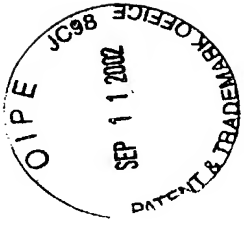


FIG. 33

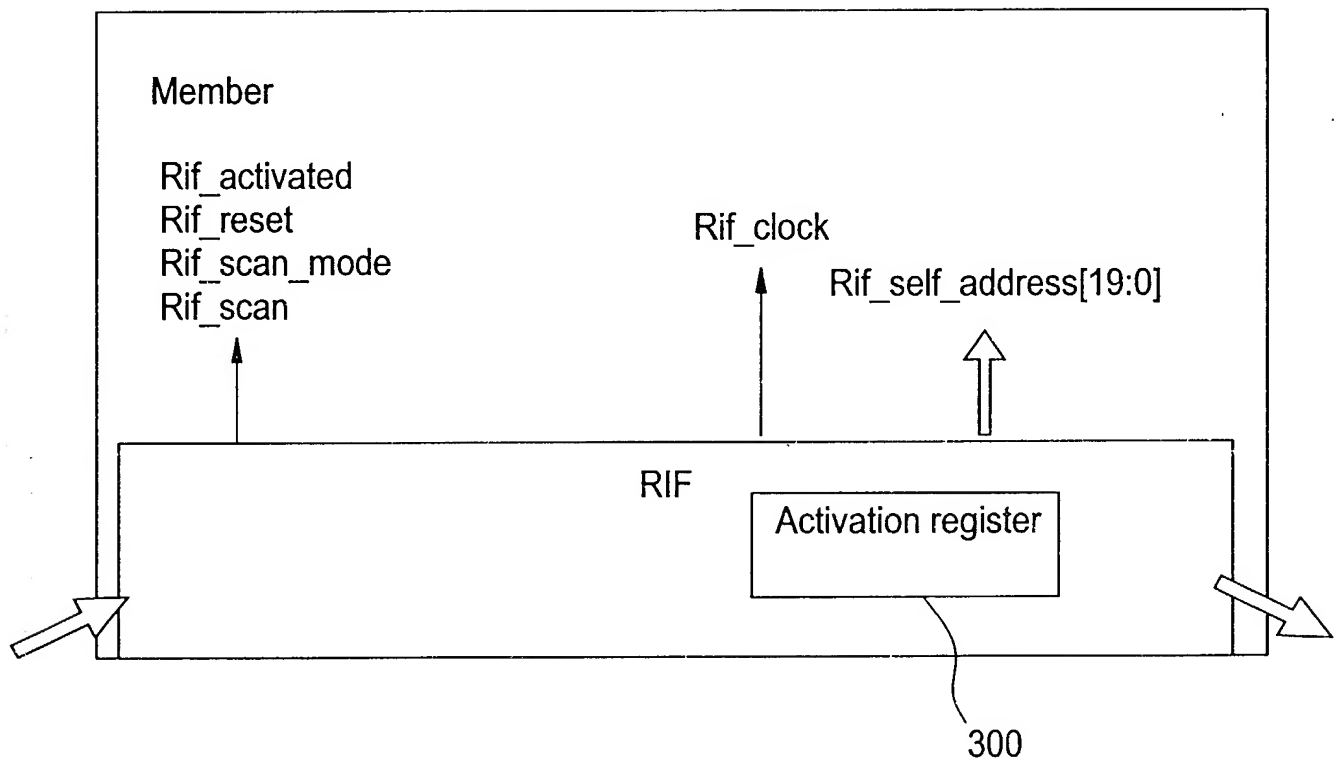


FIG. 34

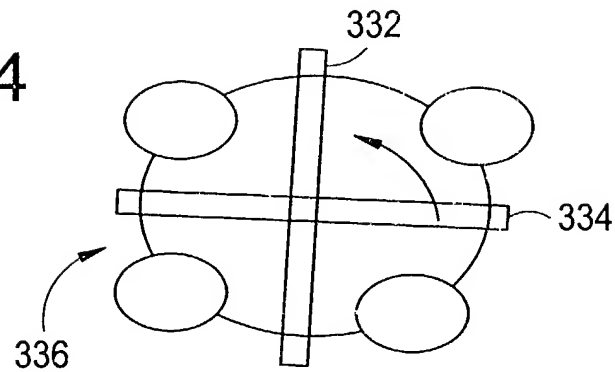


FIG. 35

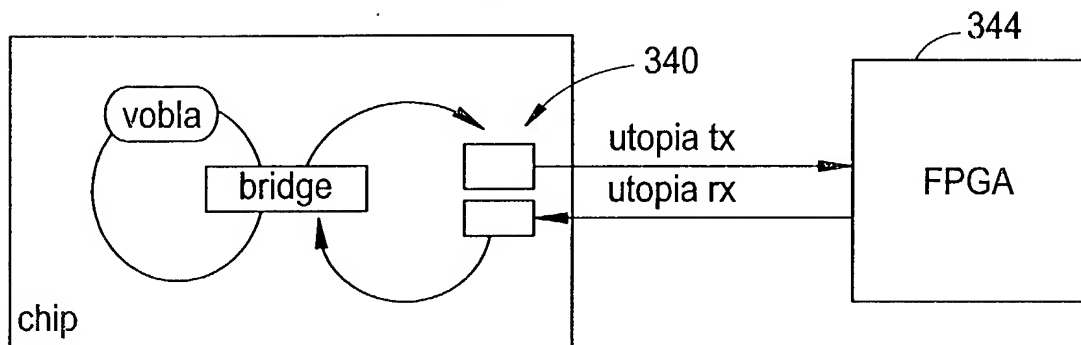


FIG. 36

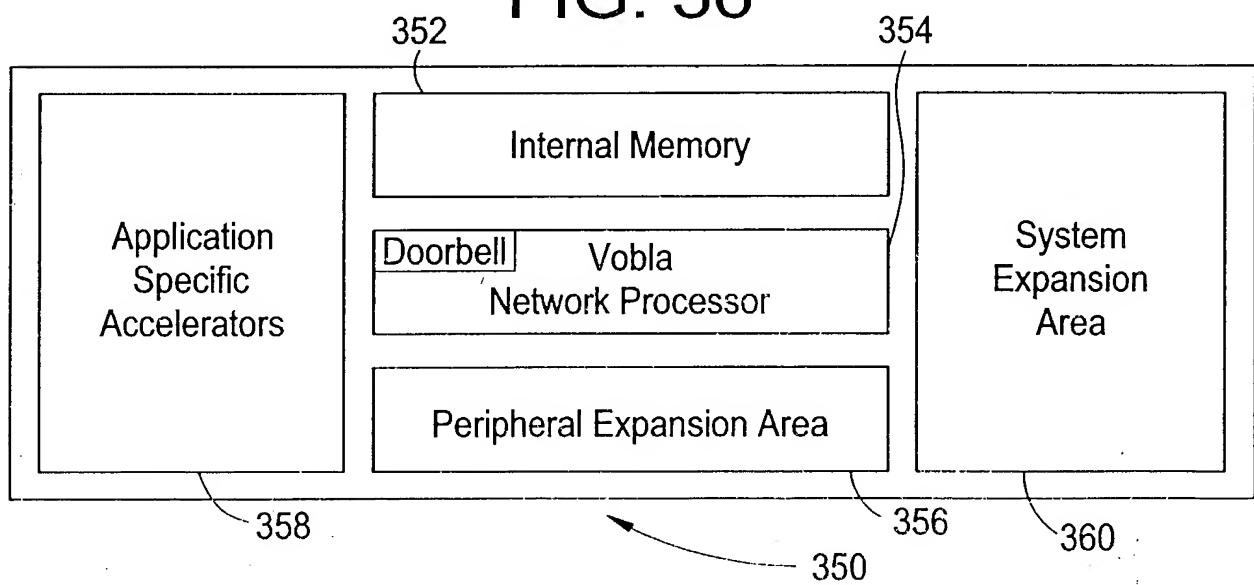


FIG. 37

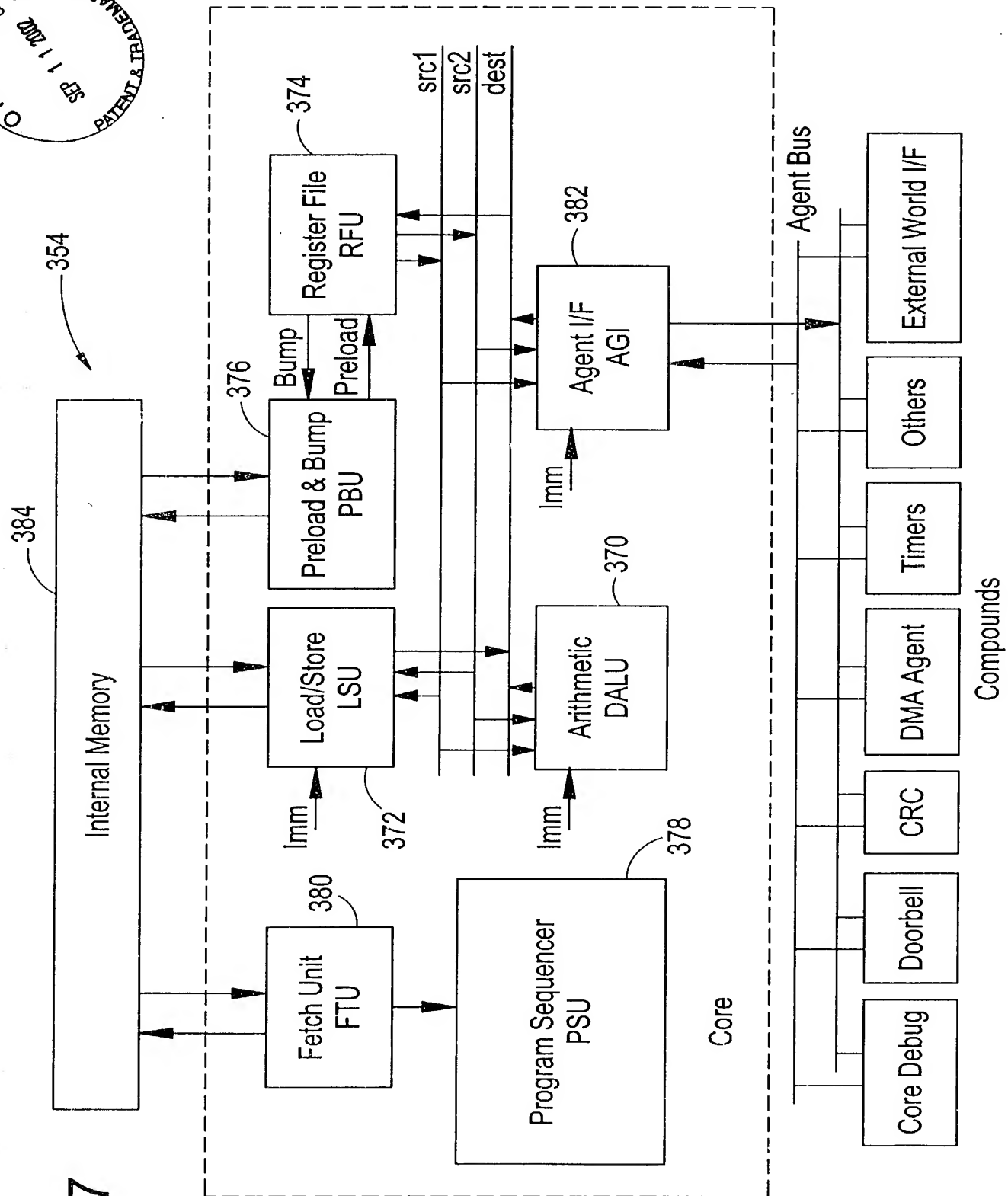
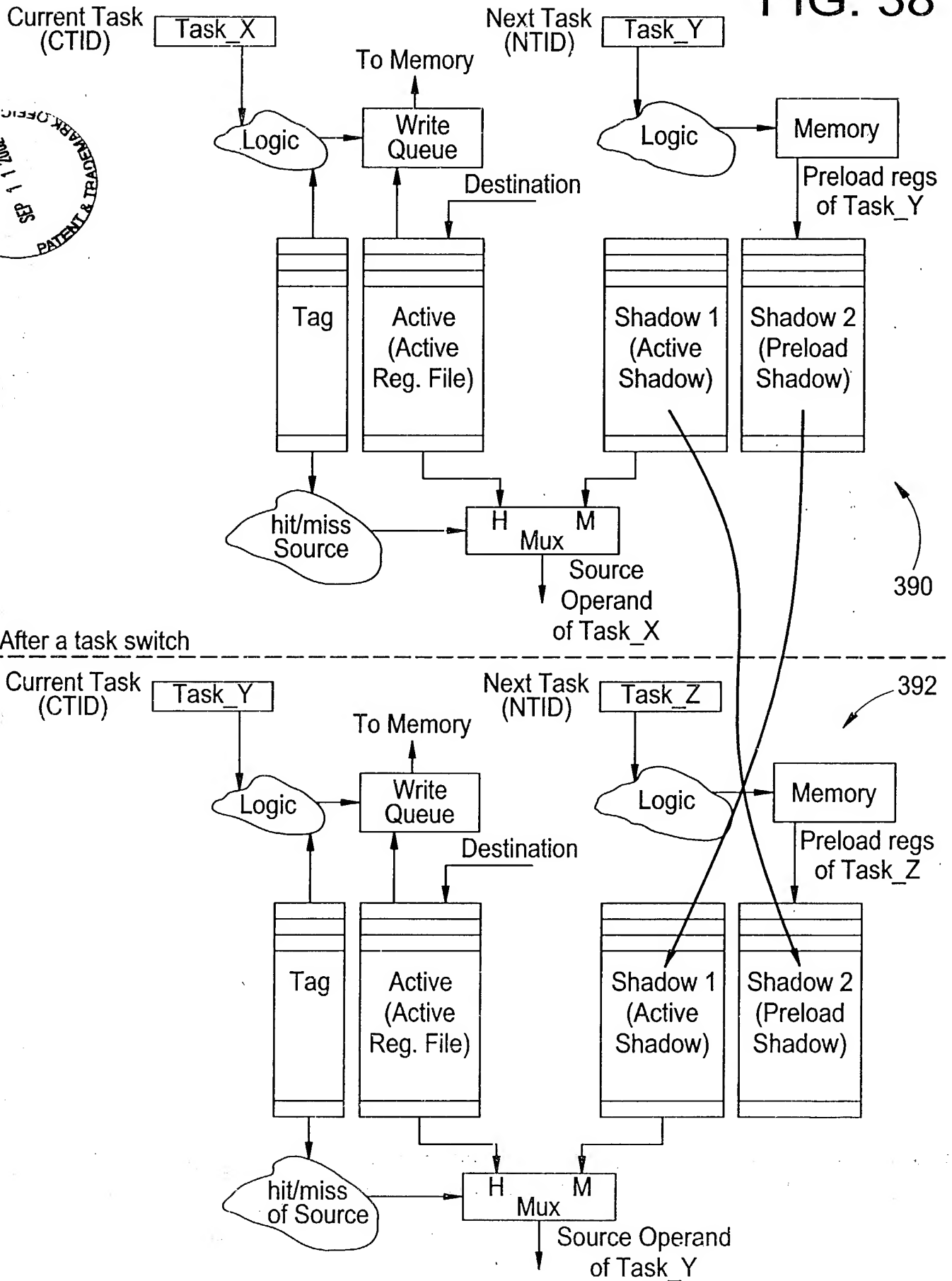
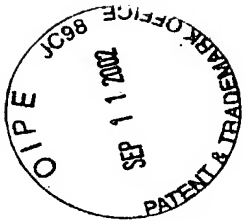


FIG. 38





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FIG. 39

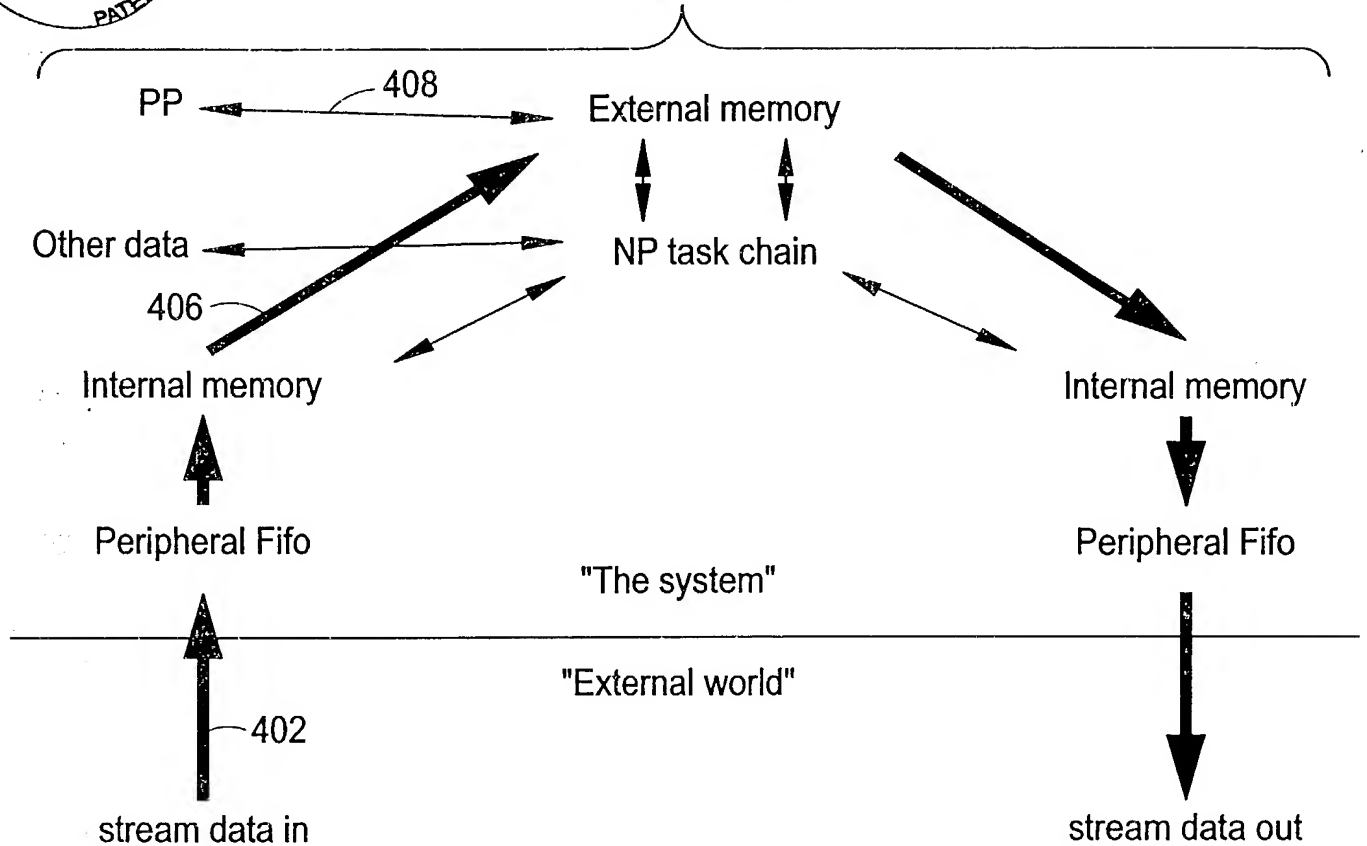
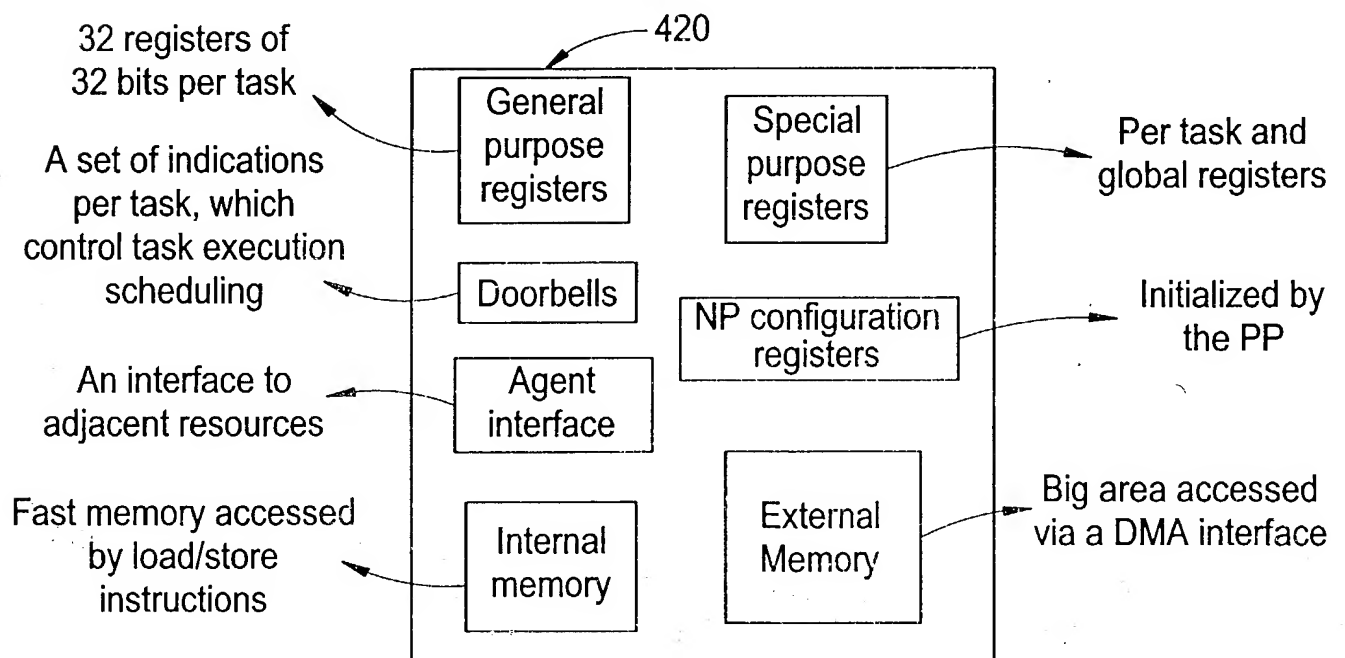


FIG. 40



3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0



mb - reflection of the RAM mult-reader busy indication

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FIG. 42

3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

REFETCH

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3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

DOOR BELL REQ			COUNT			U R	MASK		N T V		NTID		CTID
---------------------	--	--	-------	--	--	--------	------	--	-------------	--	------	--	------

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3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

[illegible]

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3 3 2 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 9 8 7 6 5 4 3 2 1 0
1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

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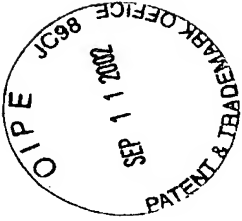


FIG. 43

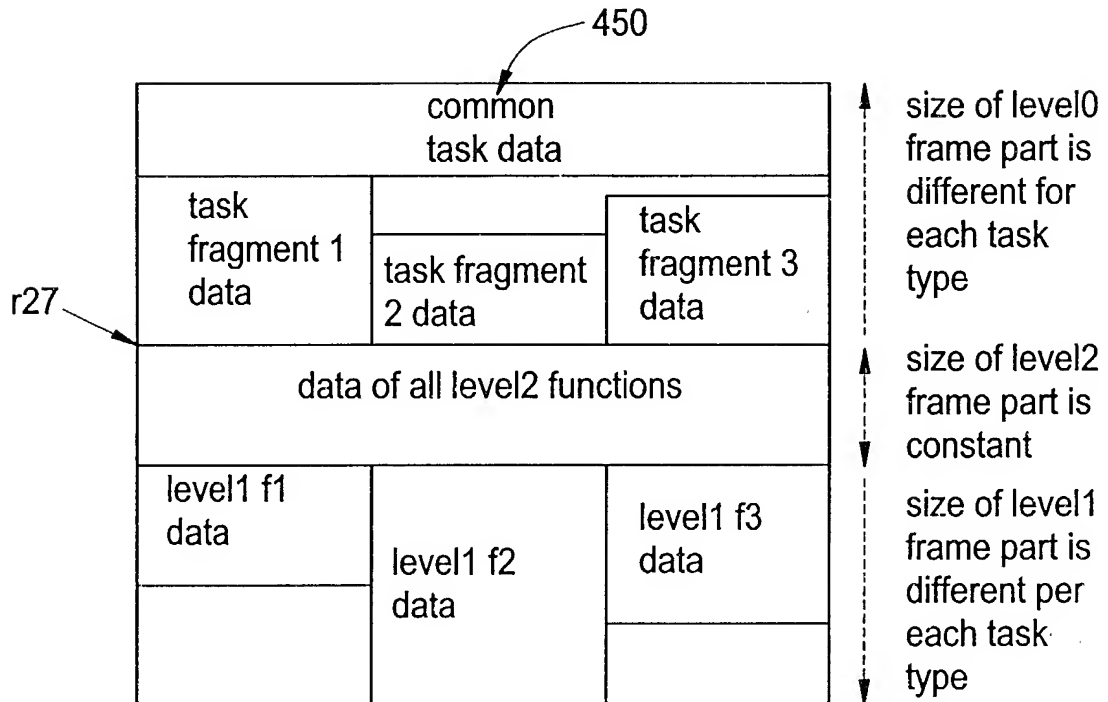
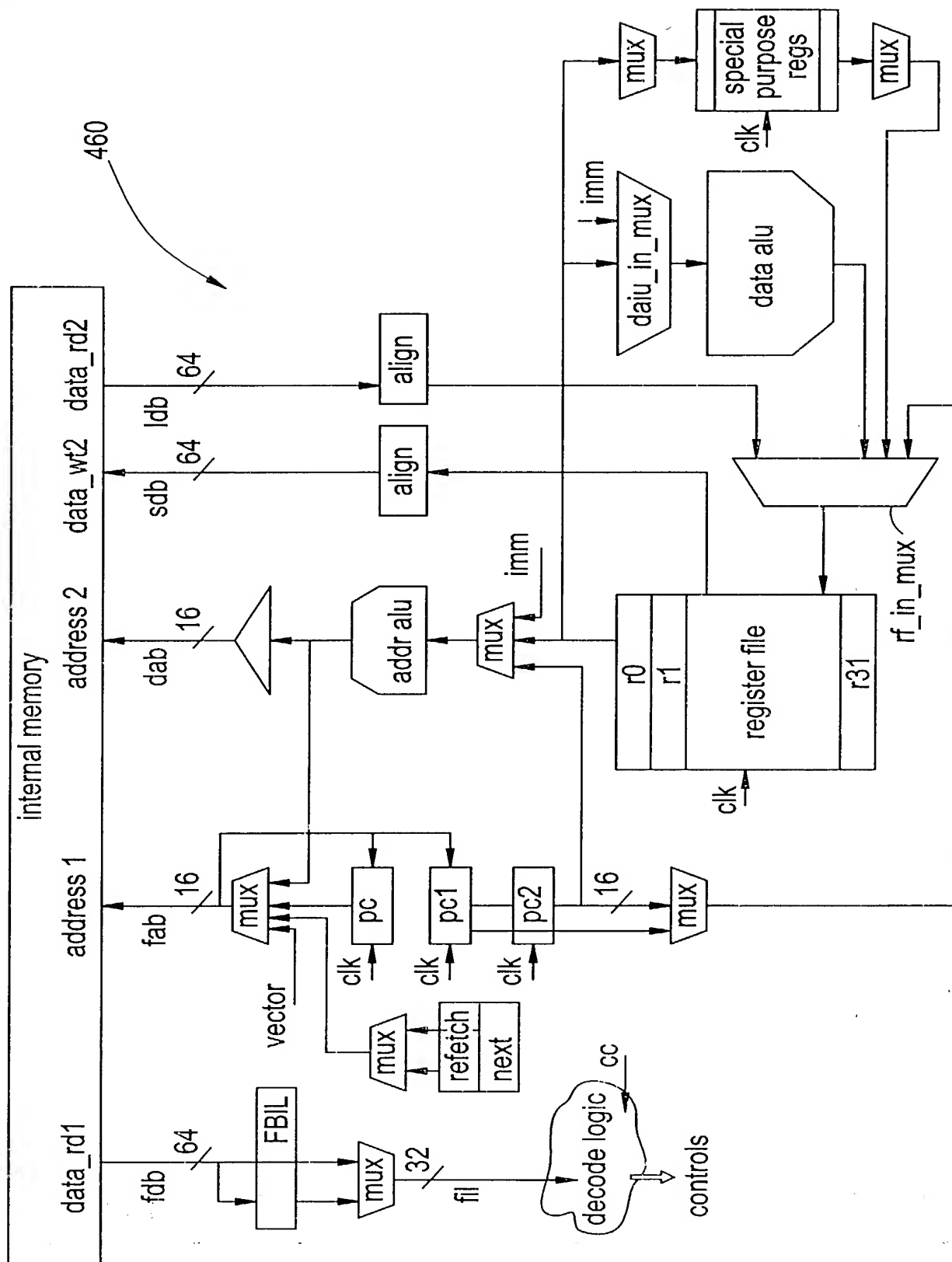


FIG. 44



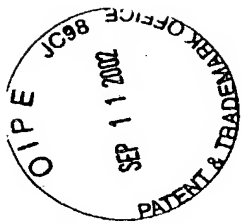
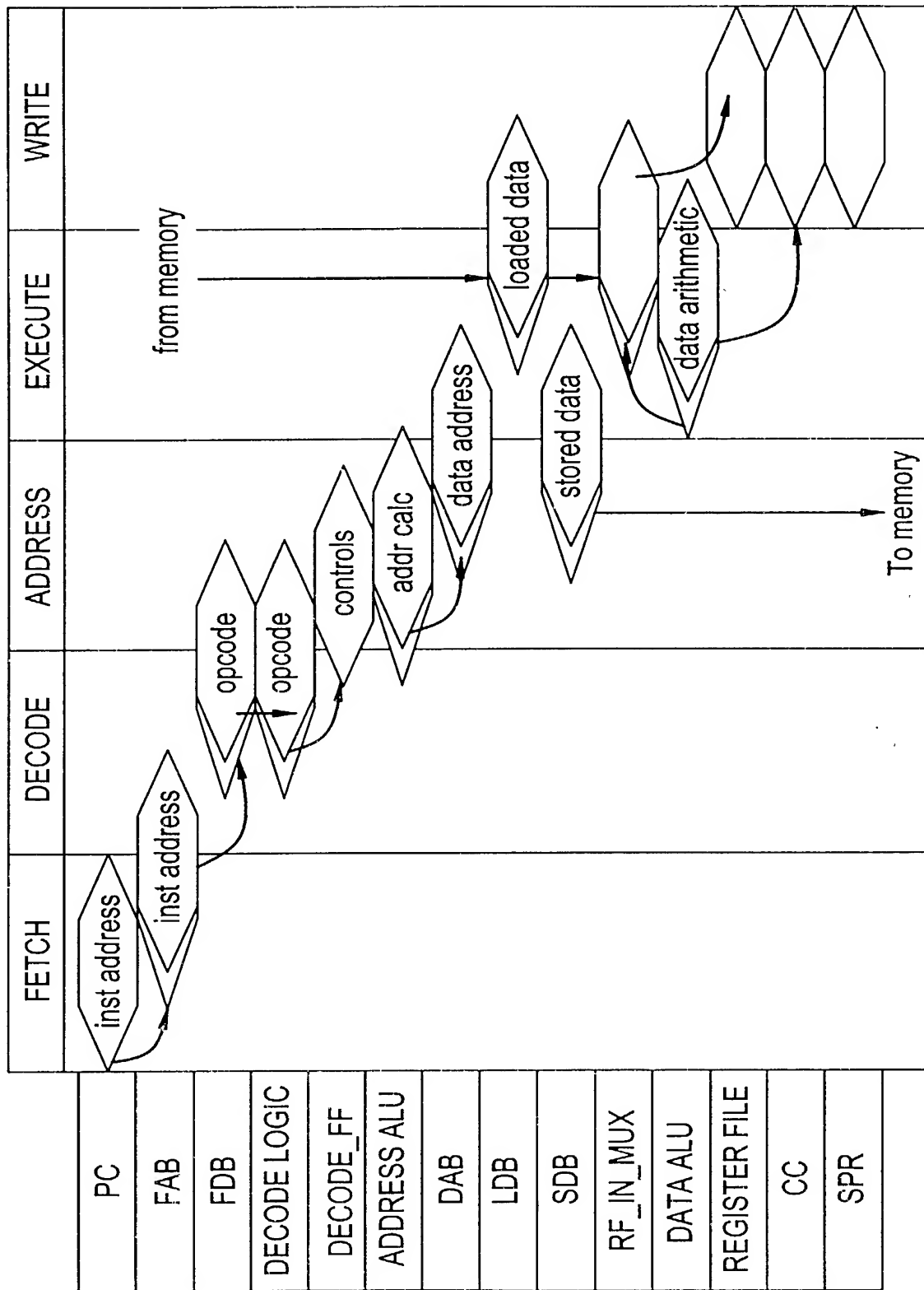


FIG. 45



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- Logic

- Flip Flop

FIG. 46

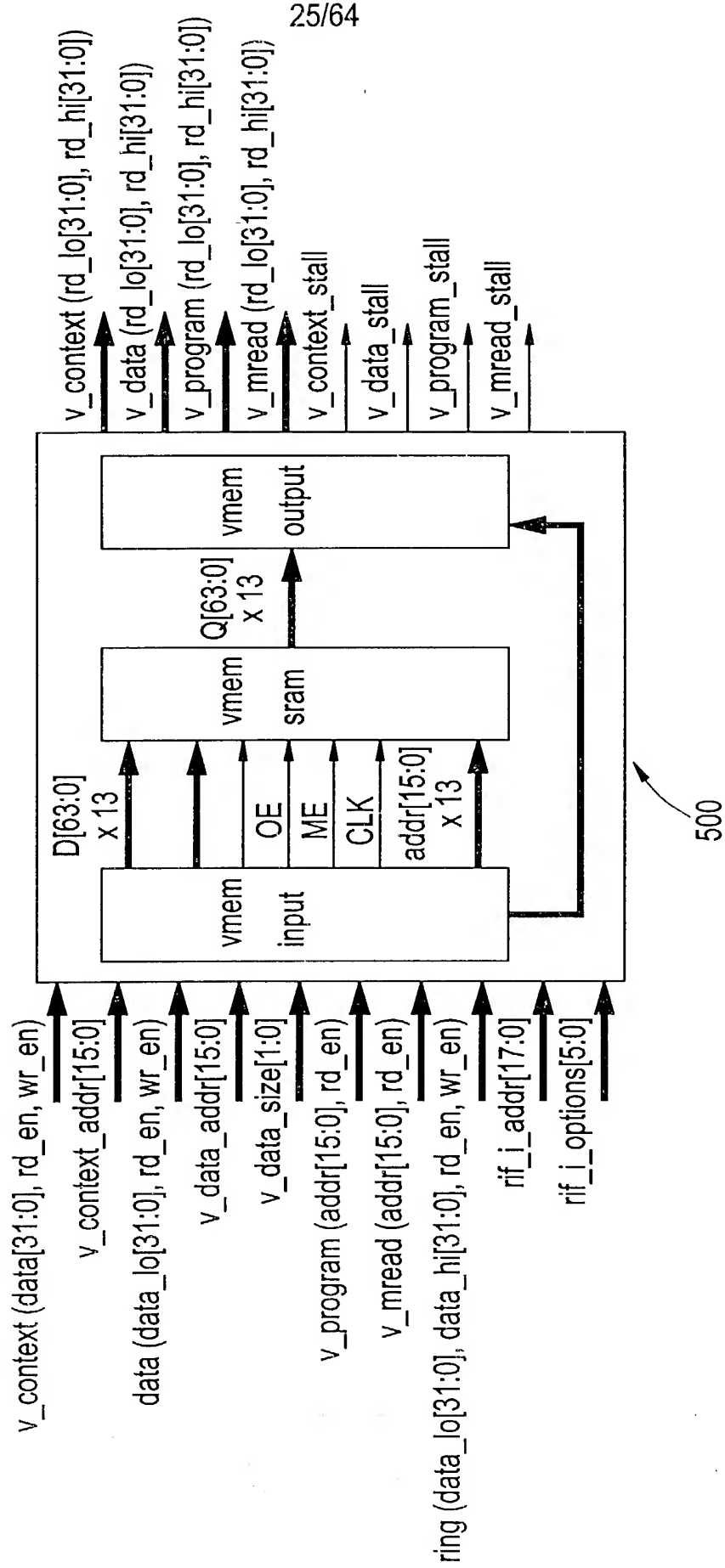




FIG. 47

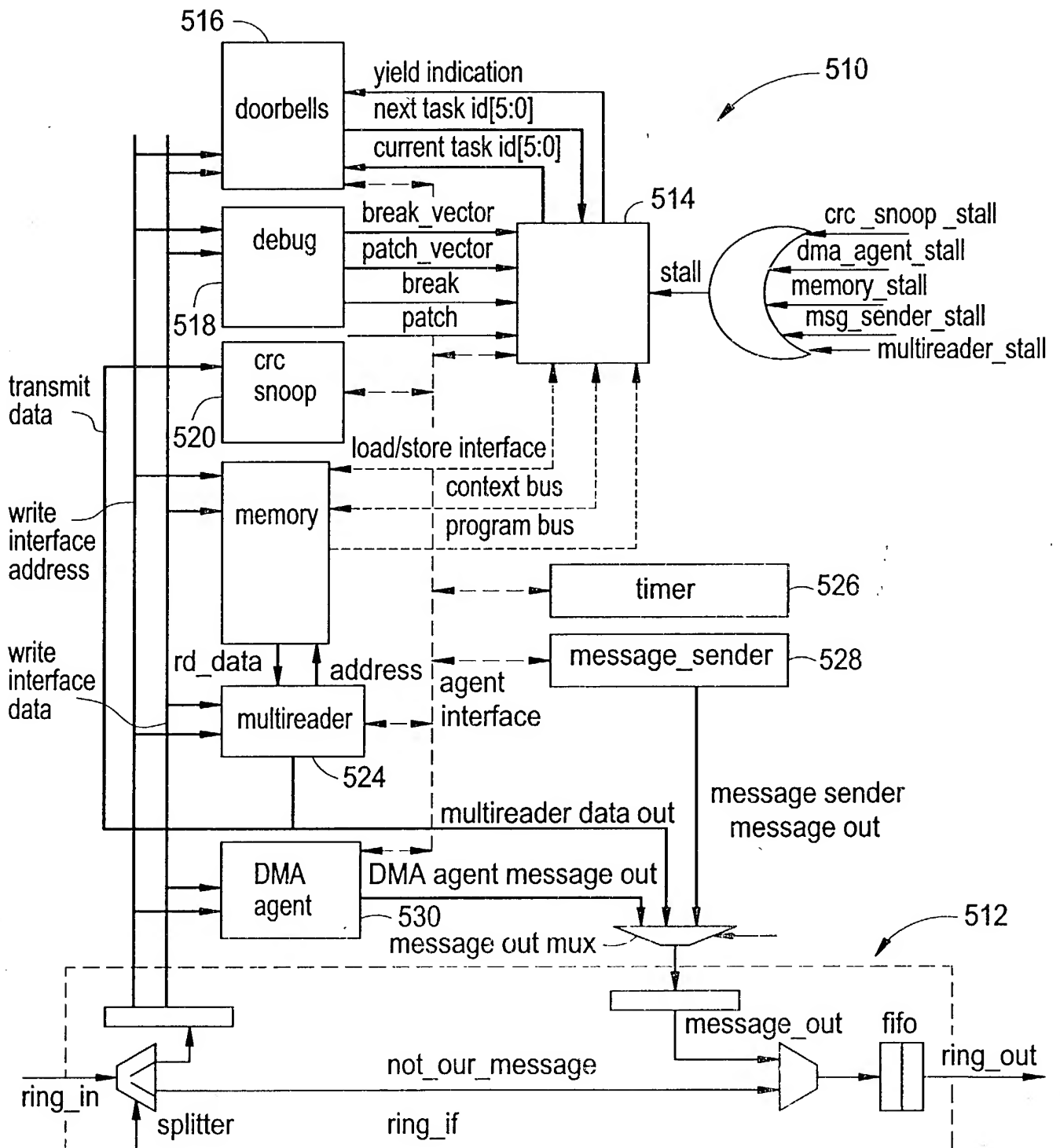


FIG. 48

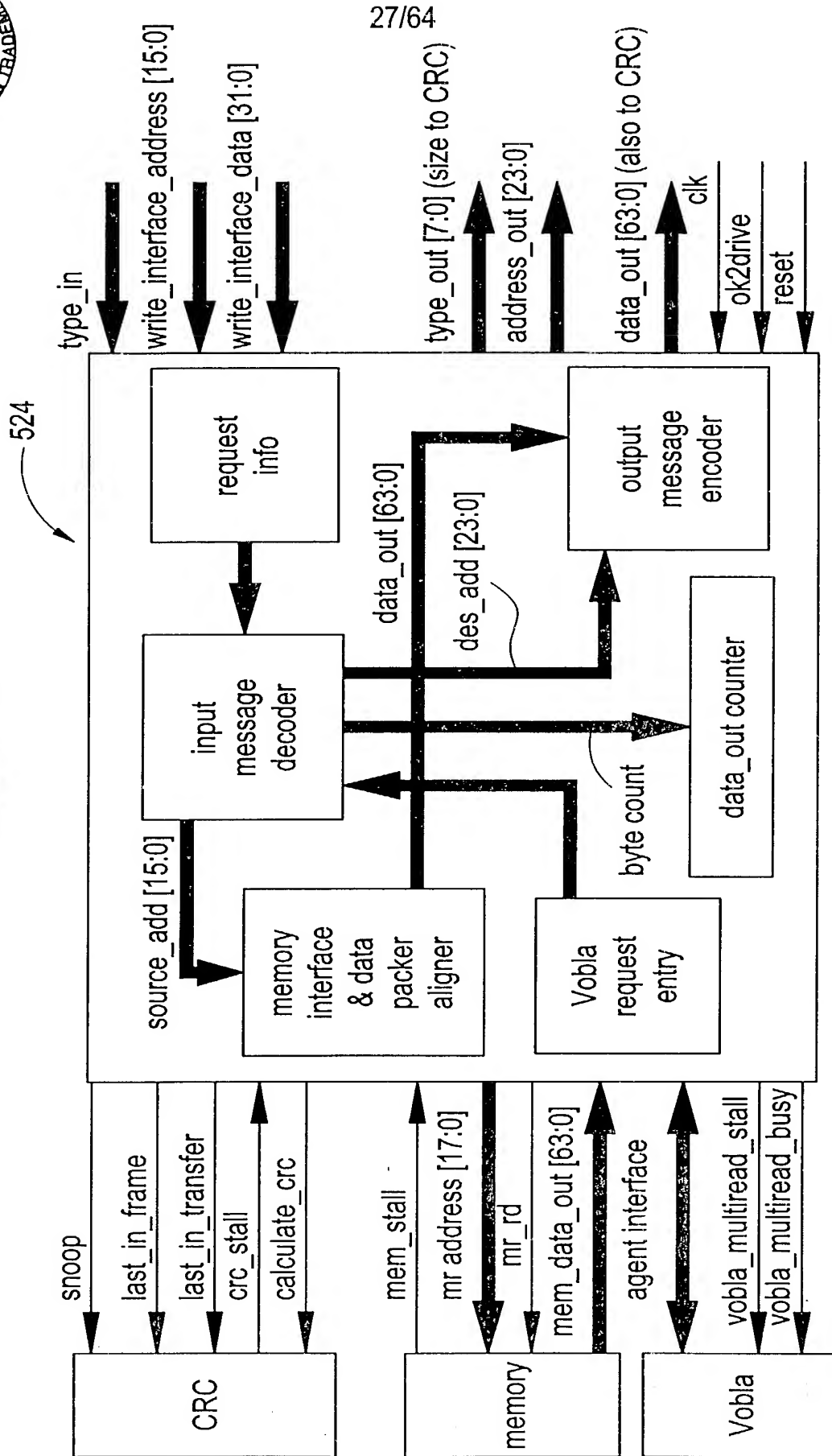
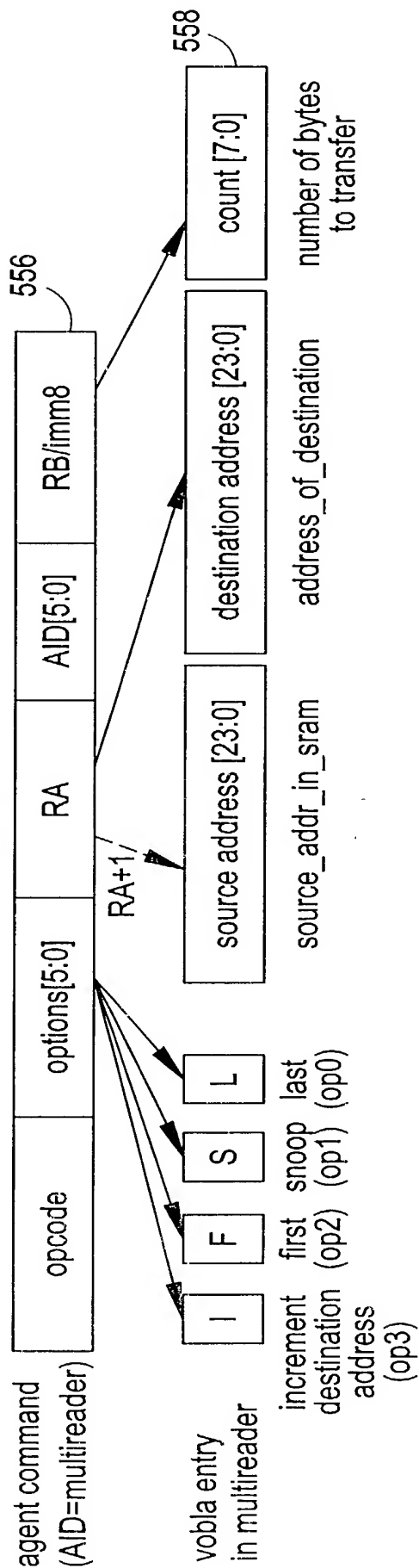
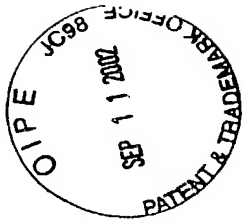


FIG. 50





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FIG. 51

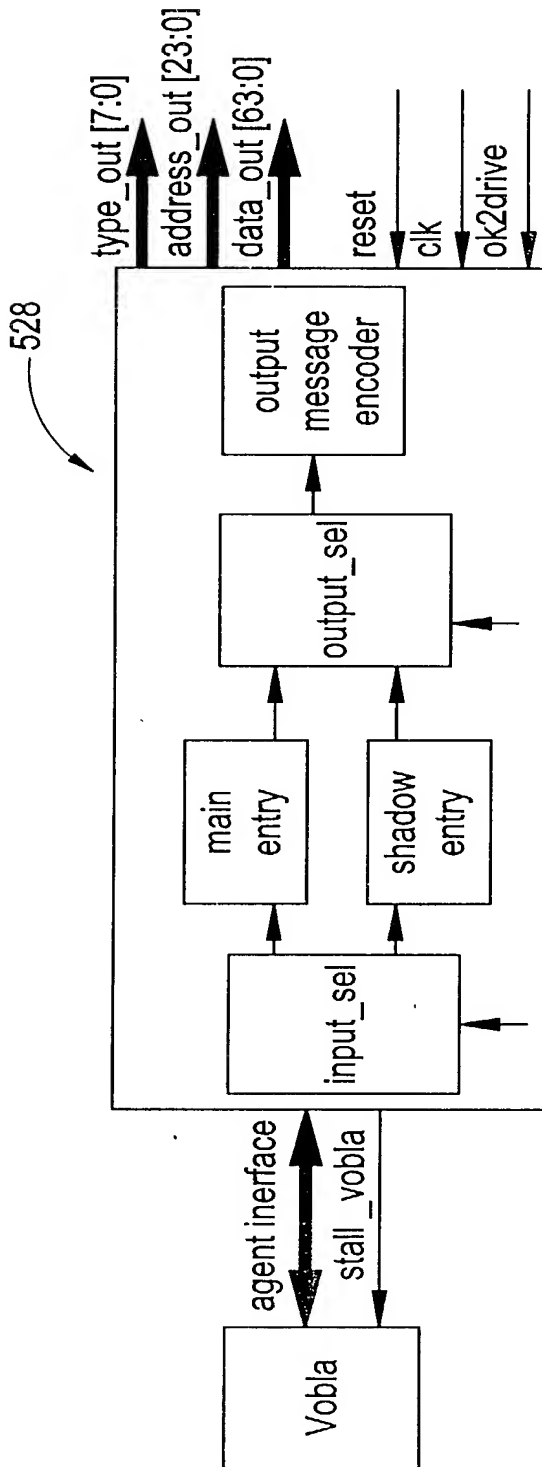




FIG. 52

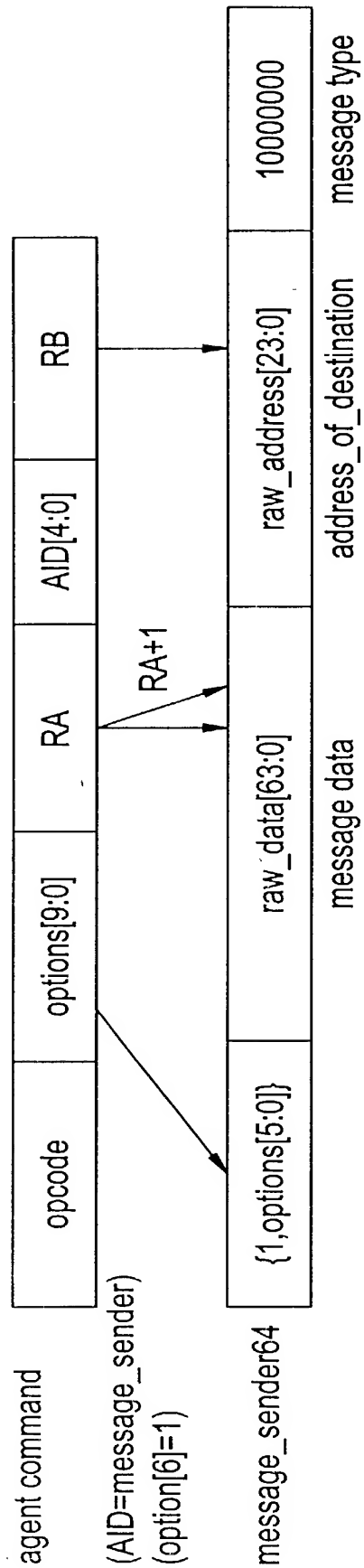
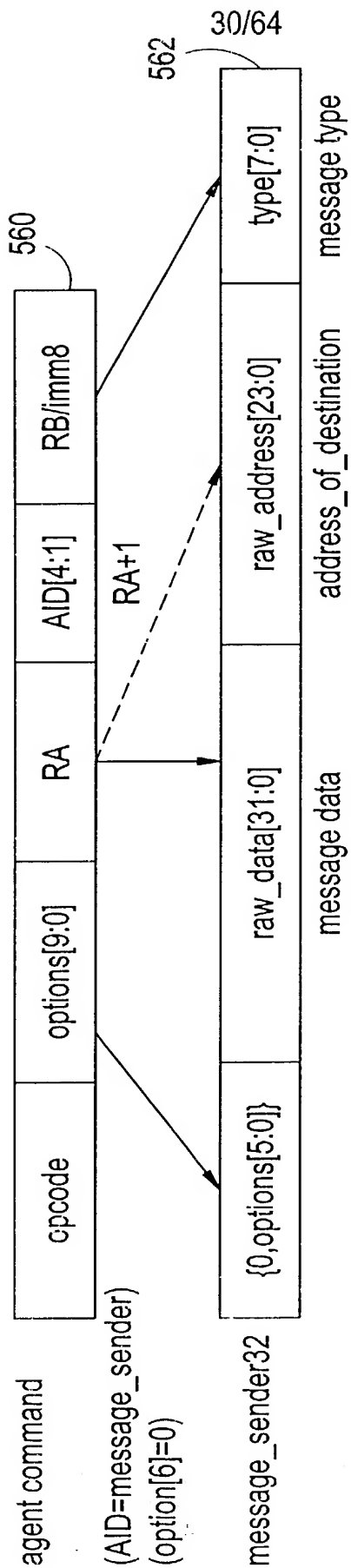
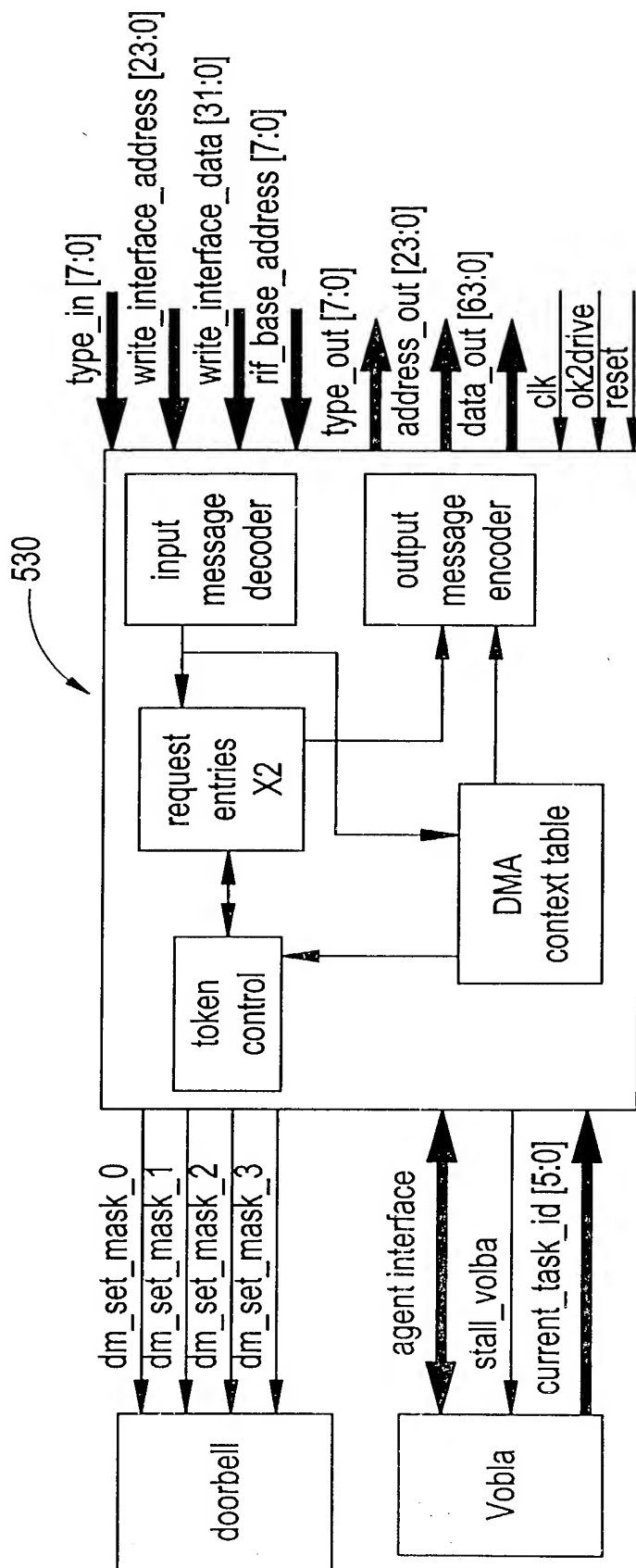


FIG. 52



FIG. 53



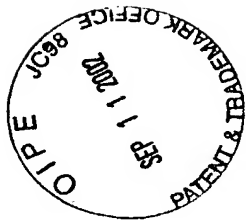
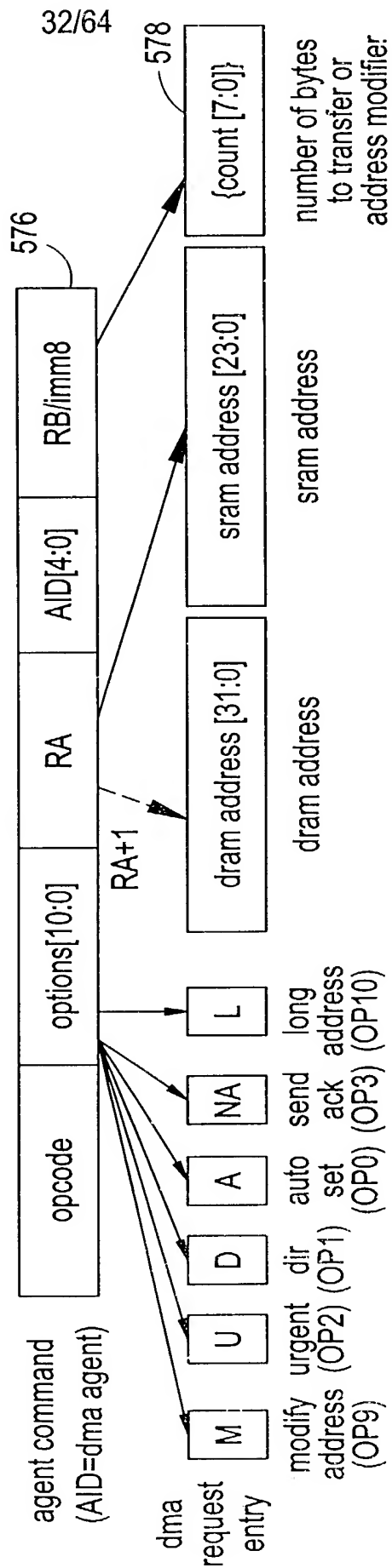
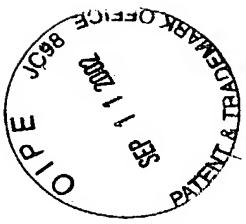


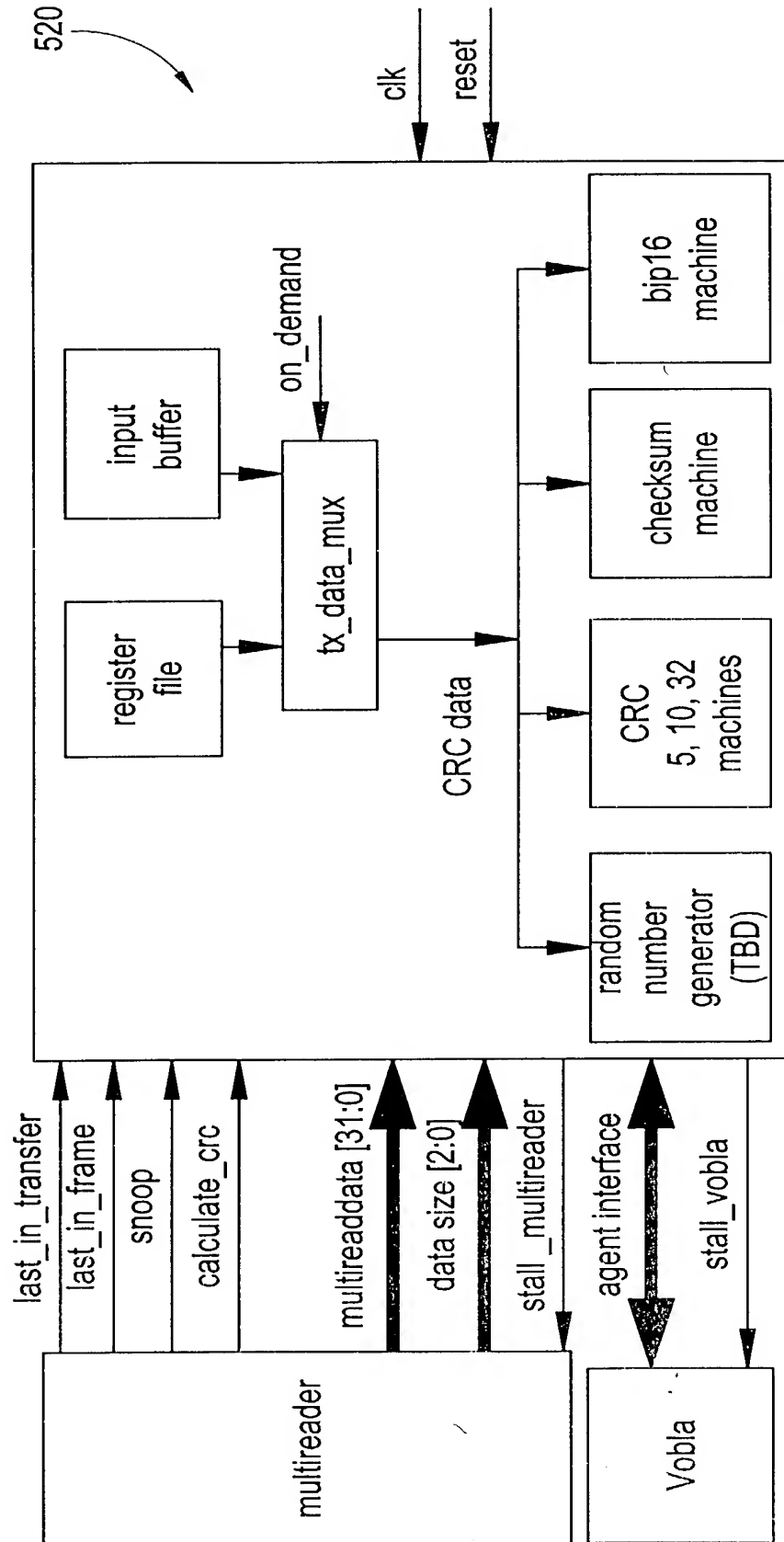
FIG. 54

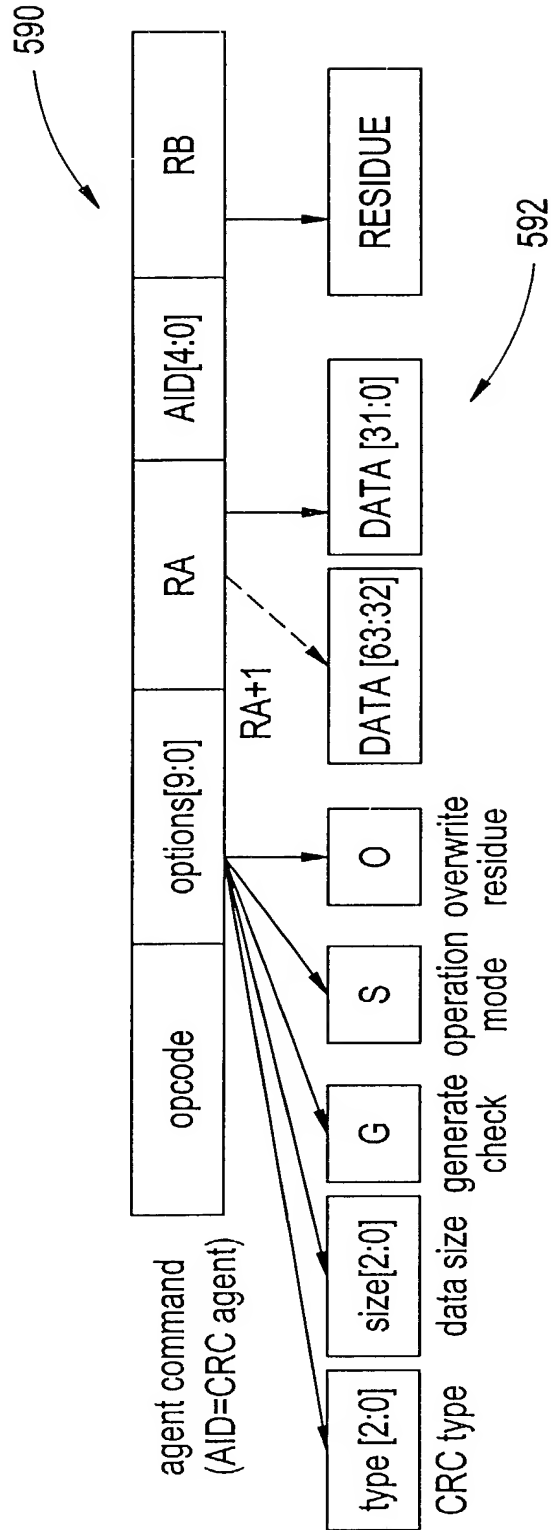




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FIG. 55







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FIG. 57

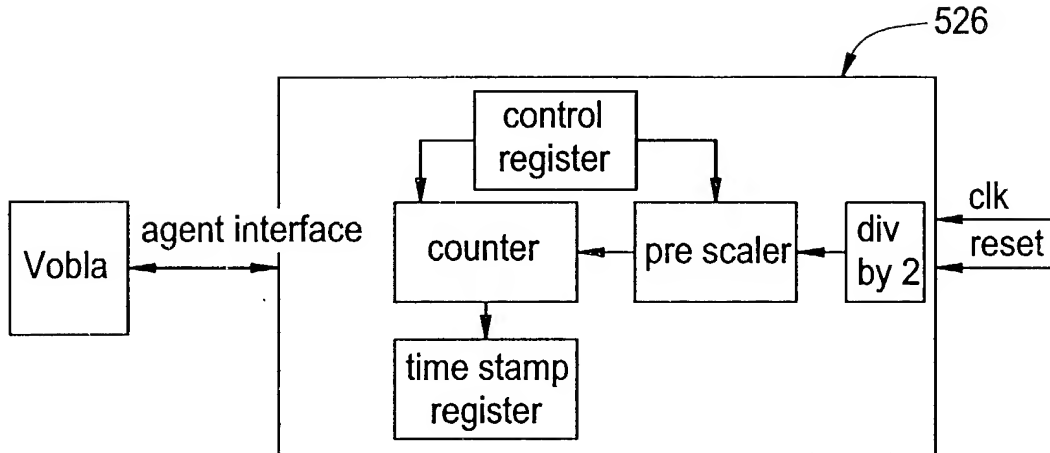
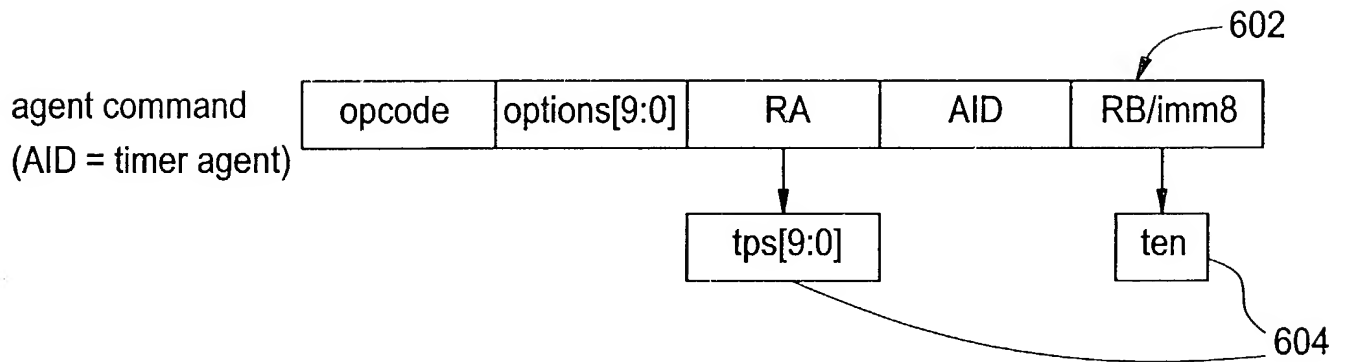


FIG. 58



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FIG. 60

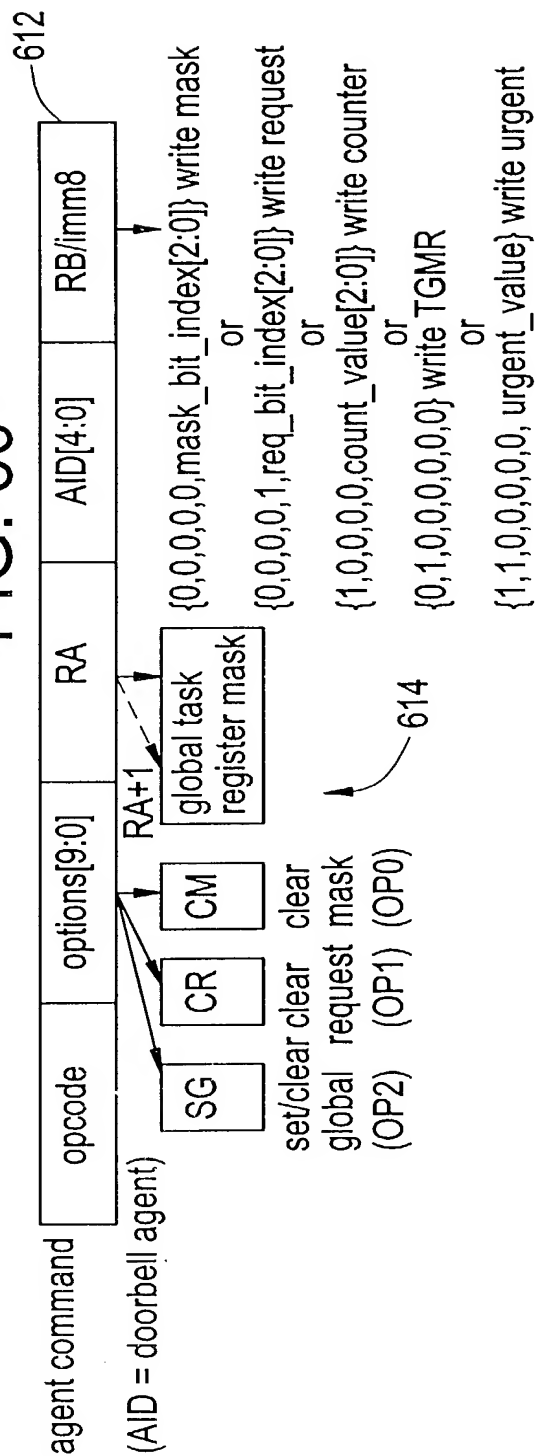


FIG. 61

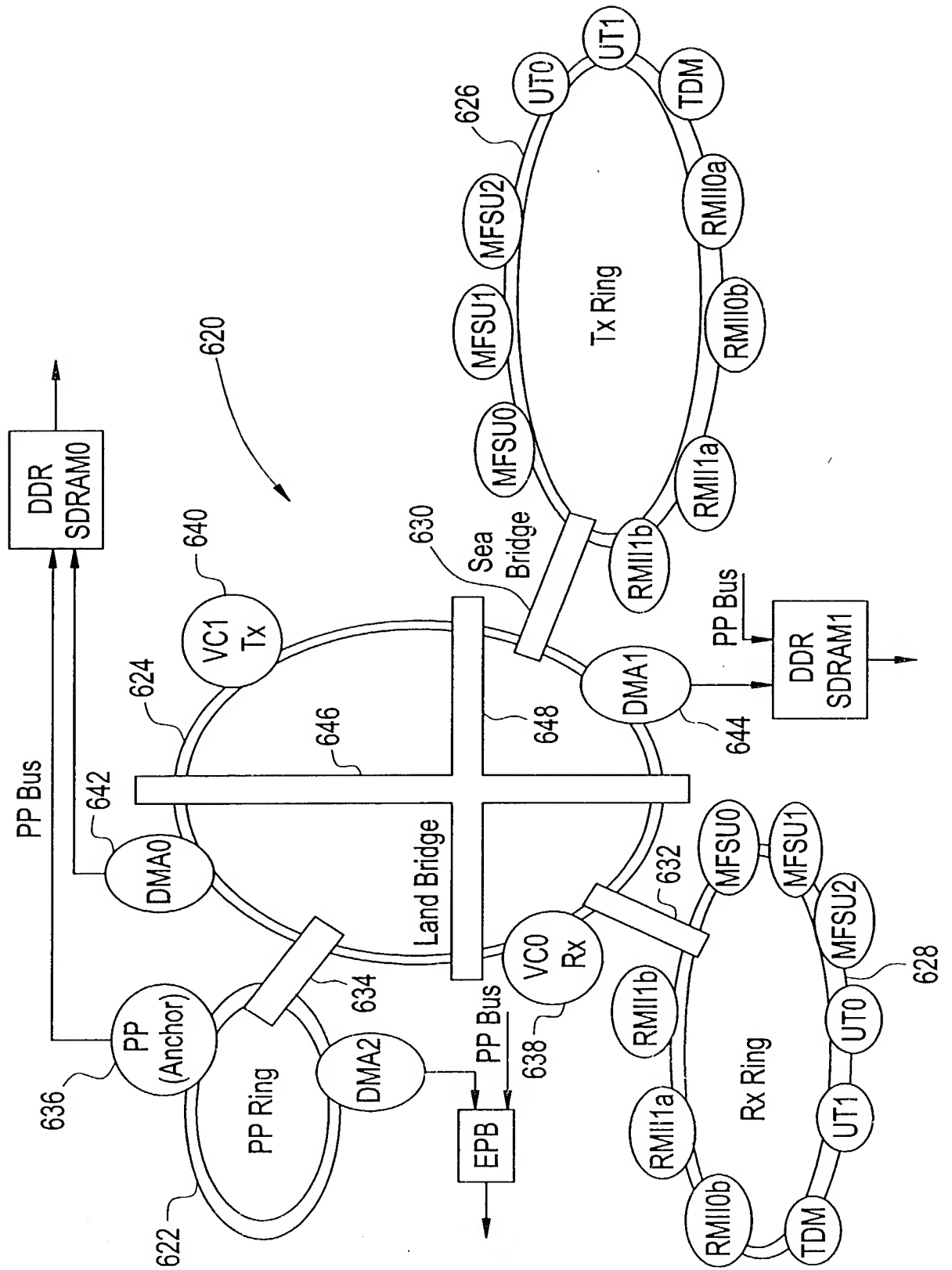
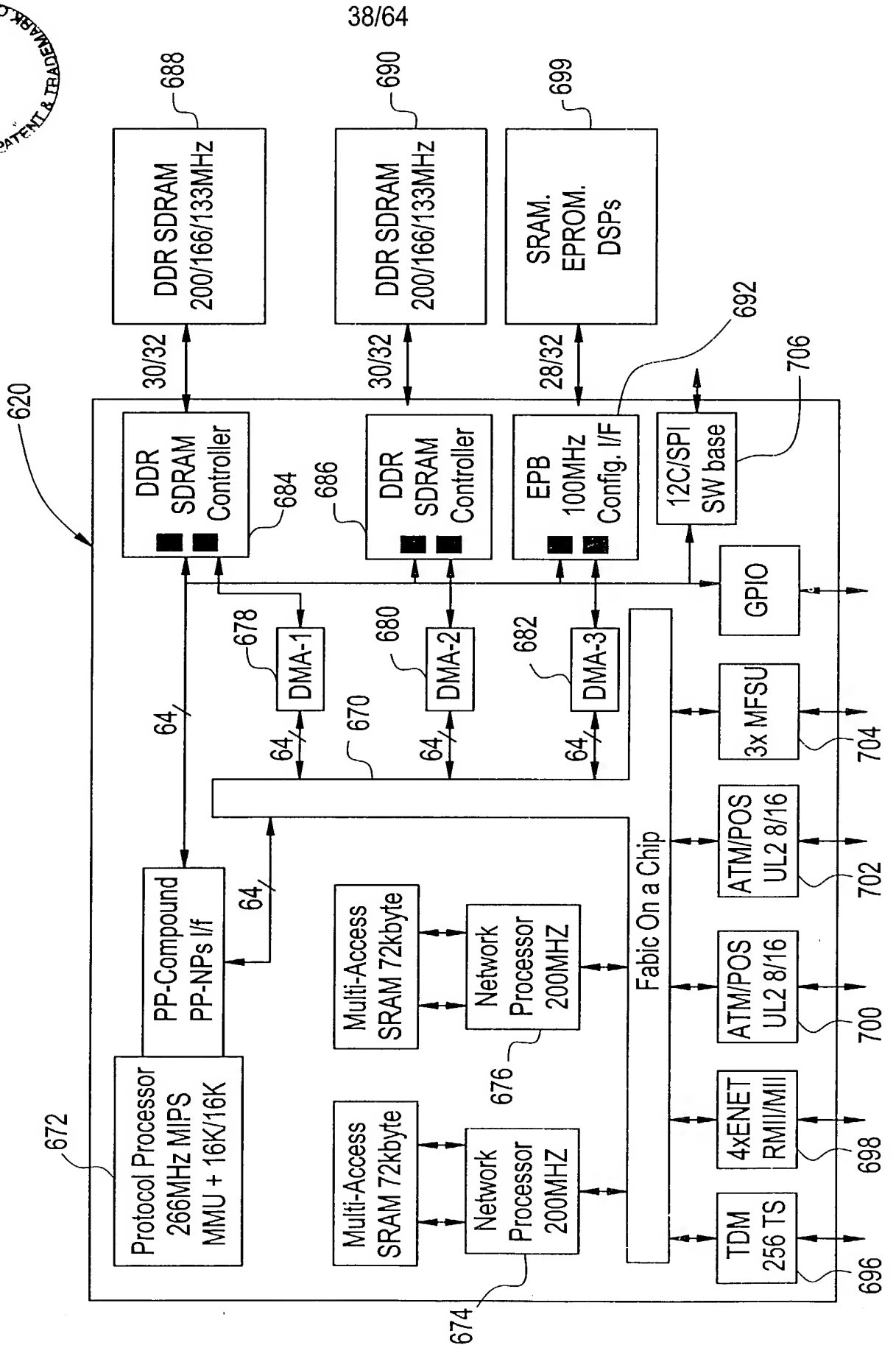


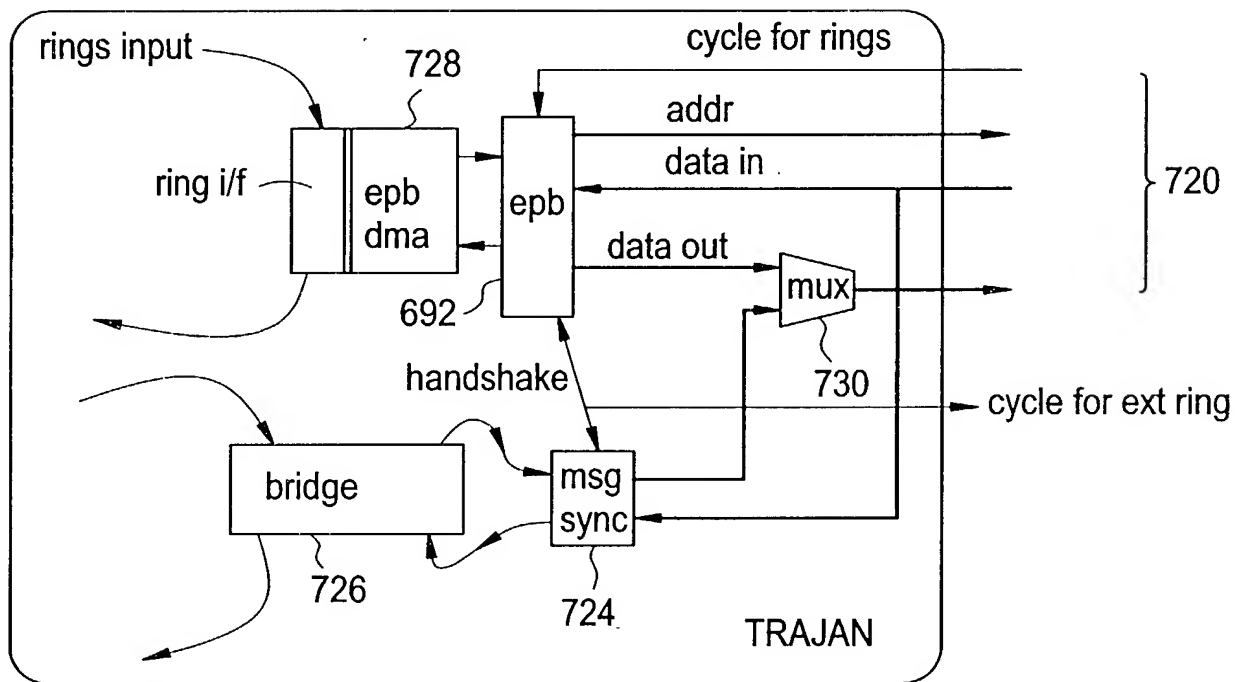
FIG. 62



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FIG. 63



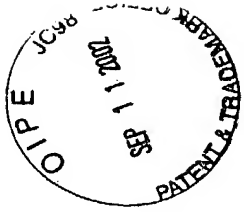


FIG. 64

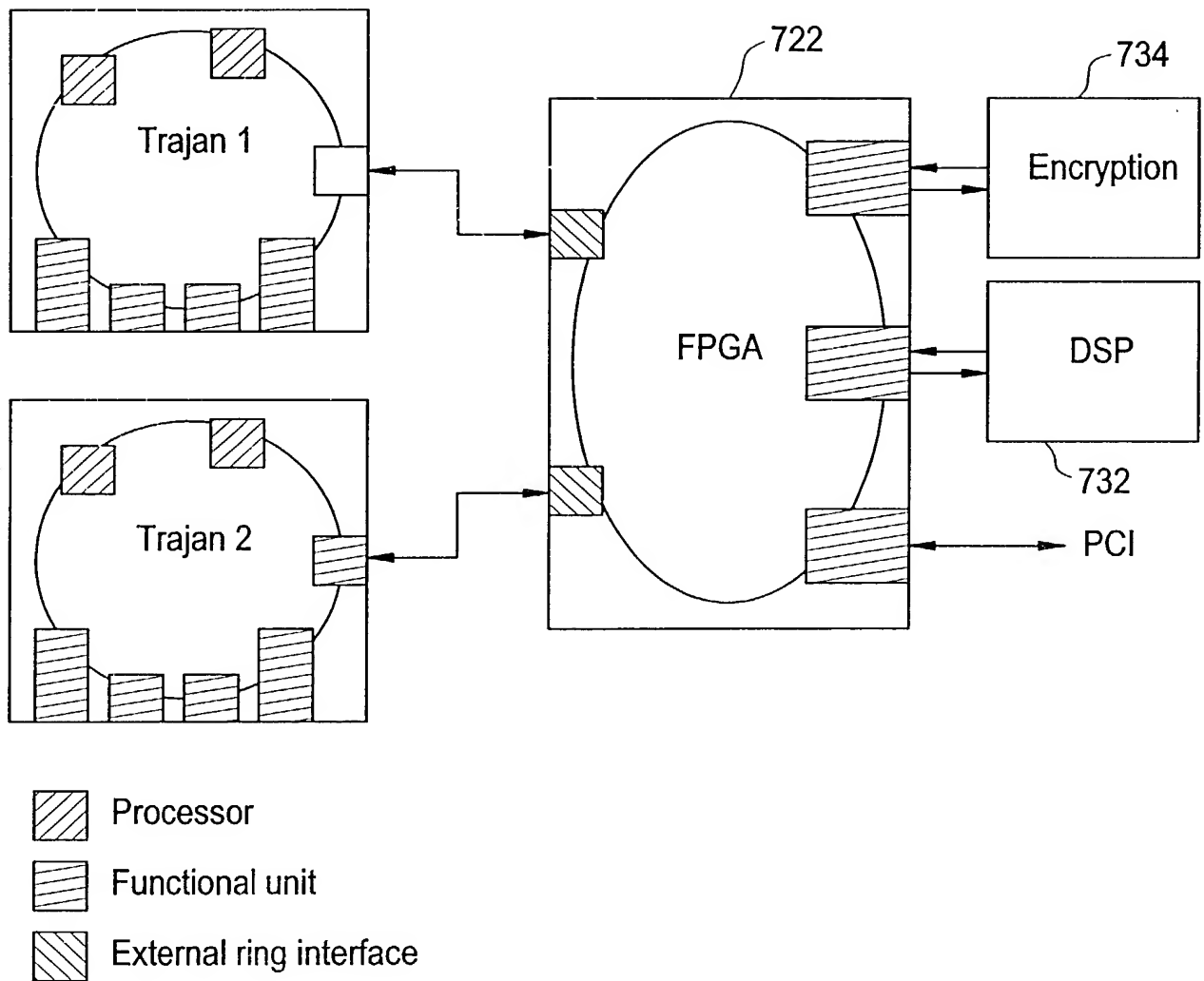




FIG. 65

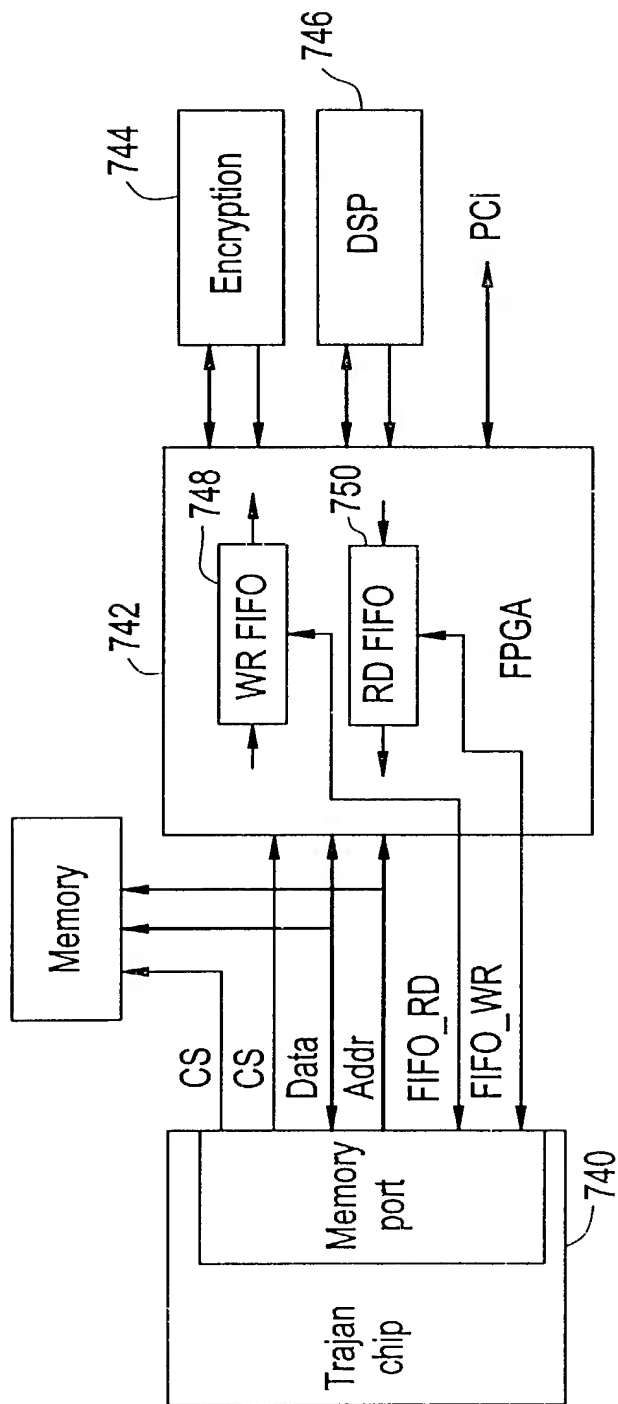


FIG. 66

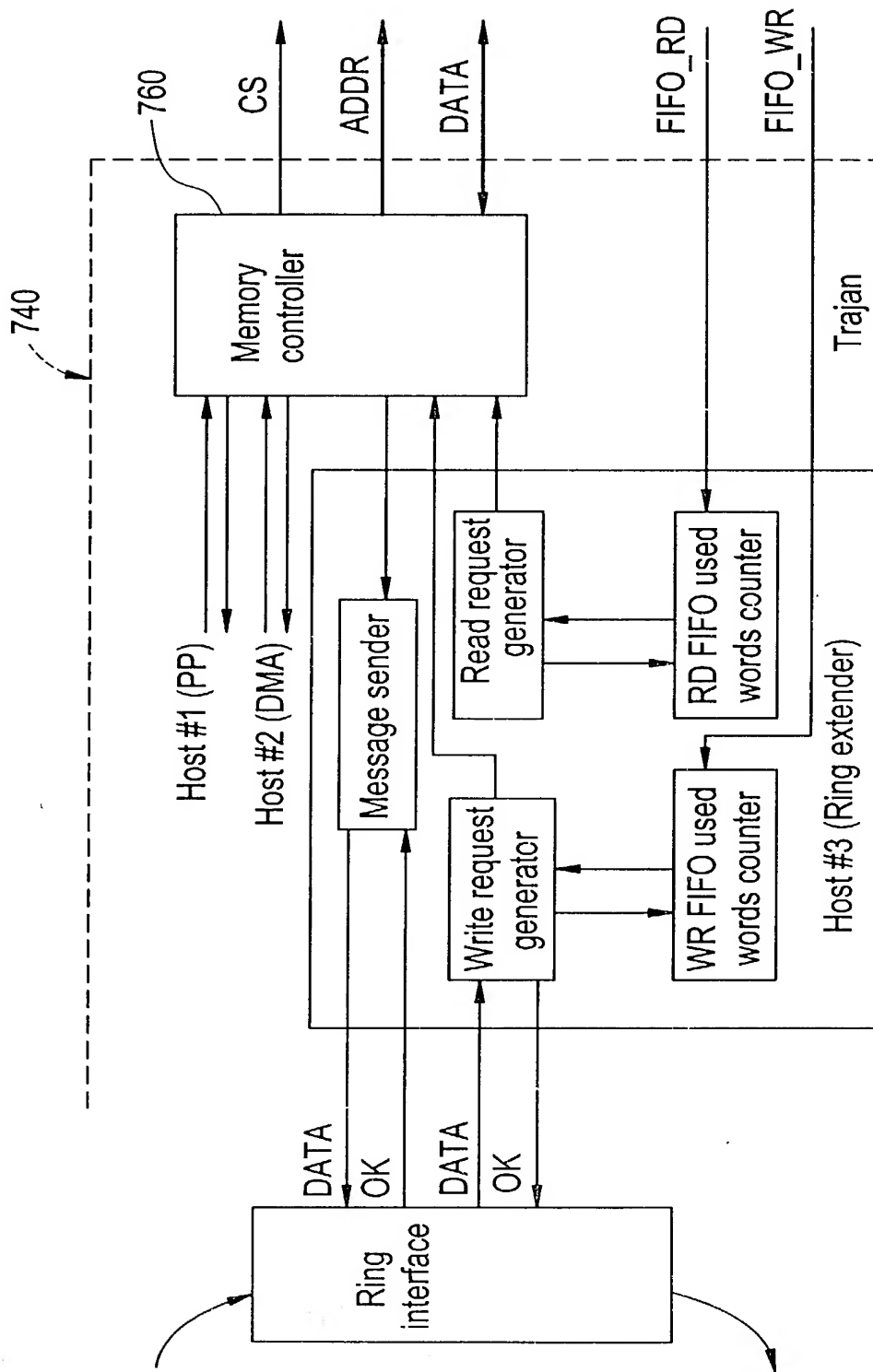


FIG. 67

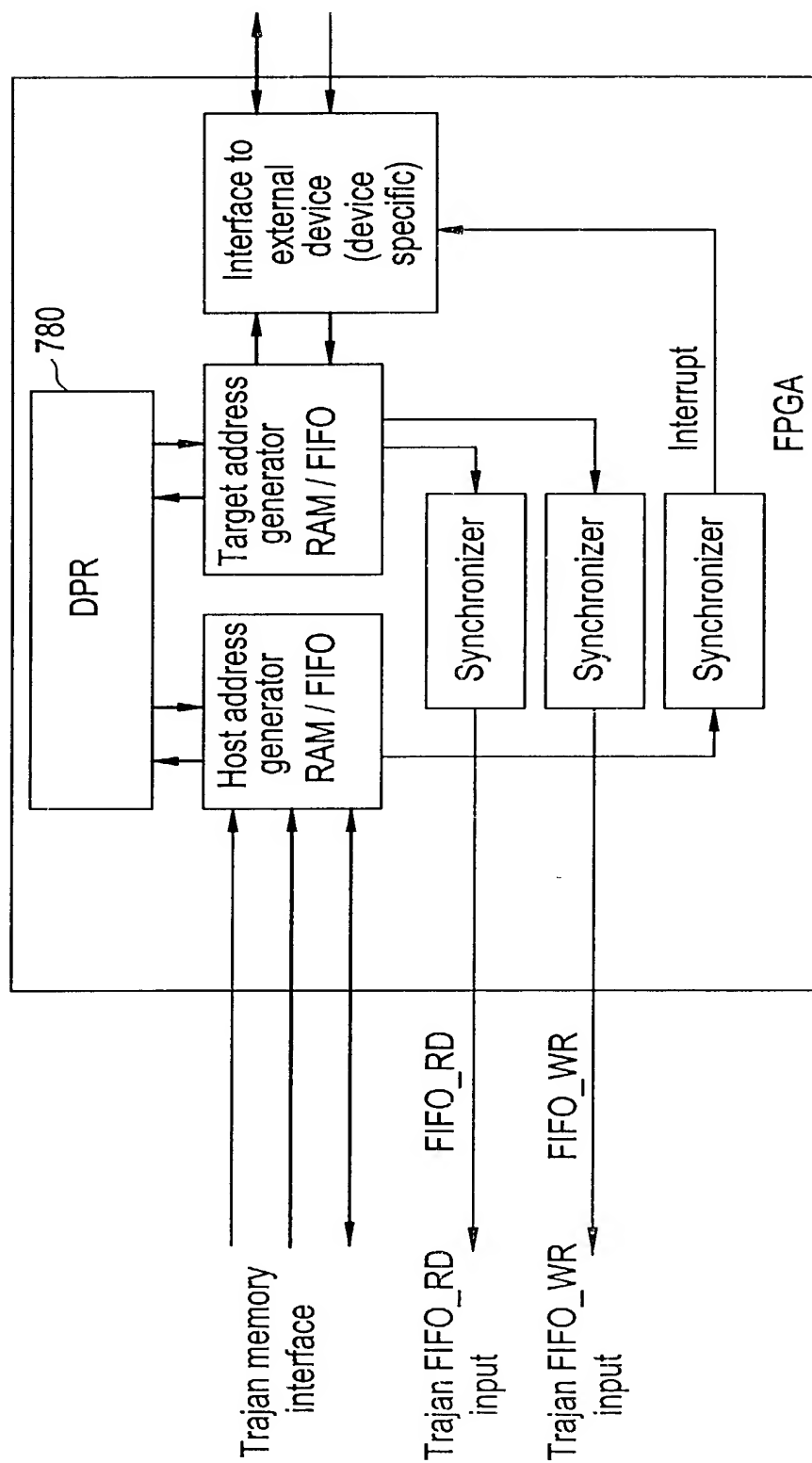
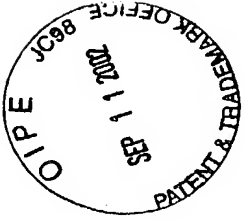
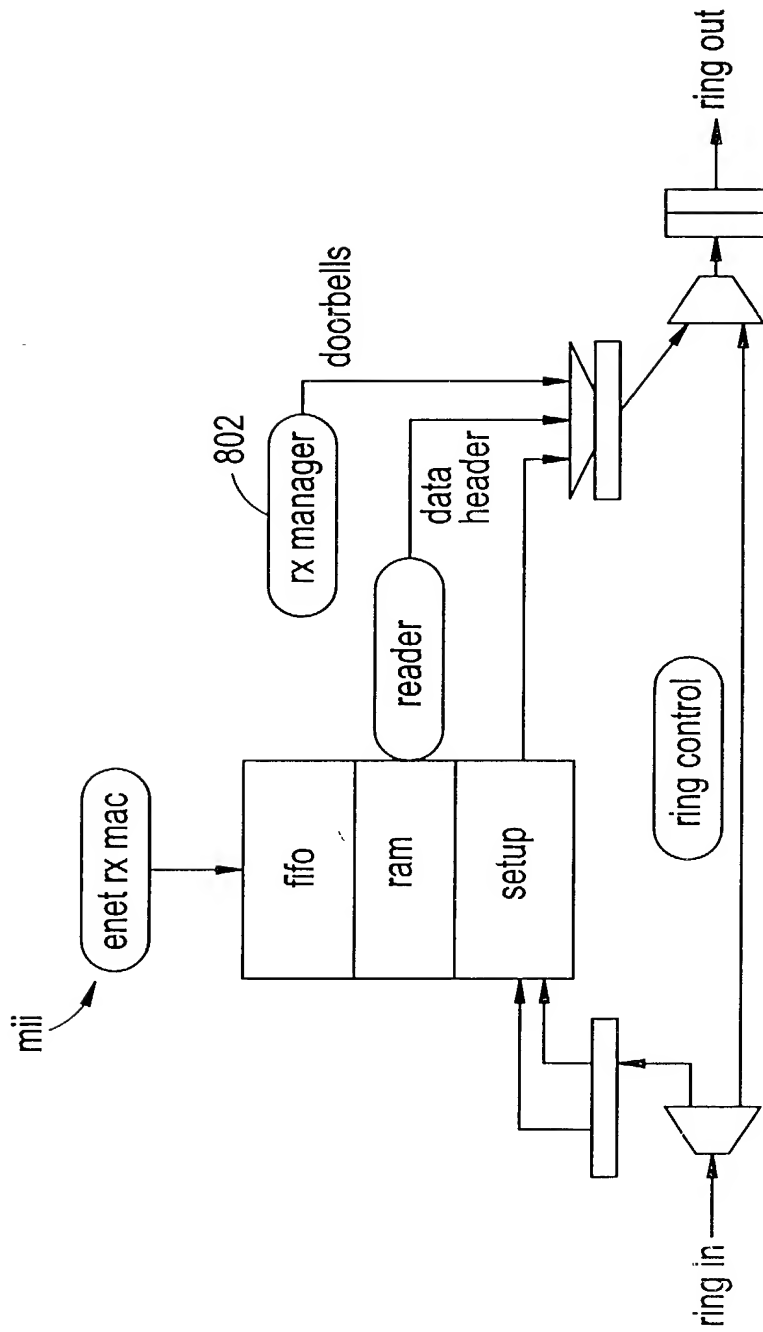


FIG. 68



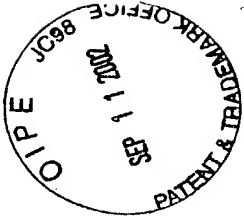
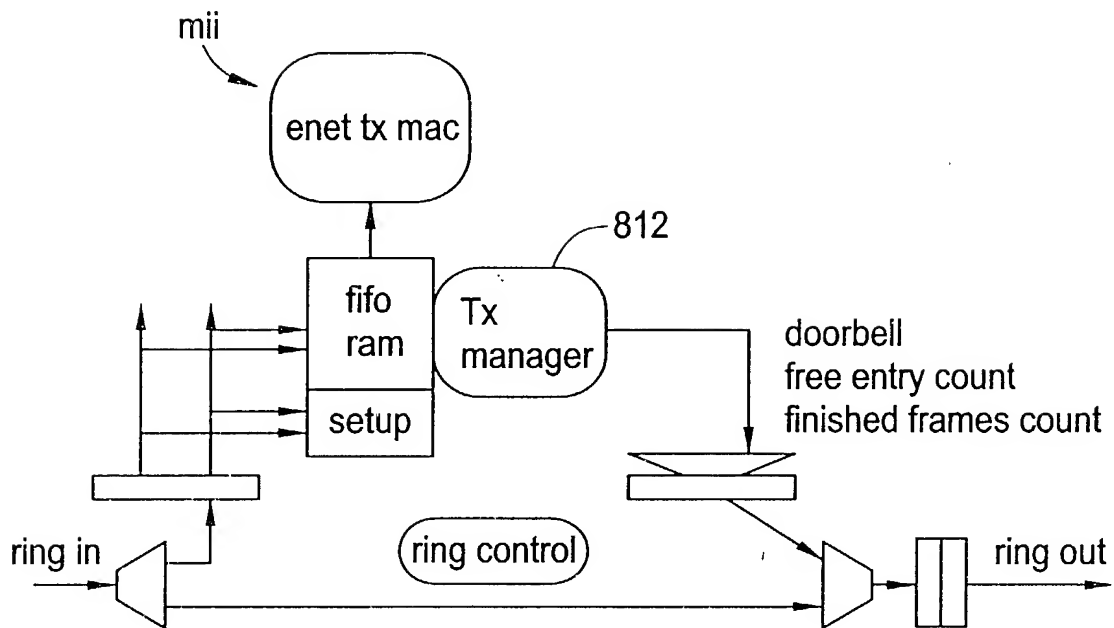


FIG. 69



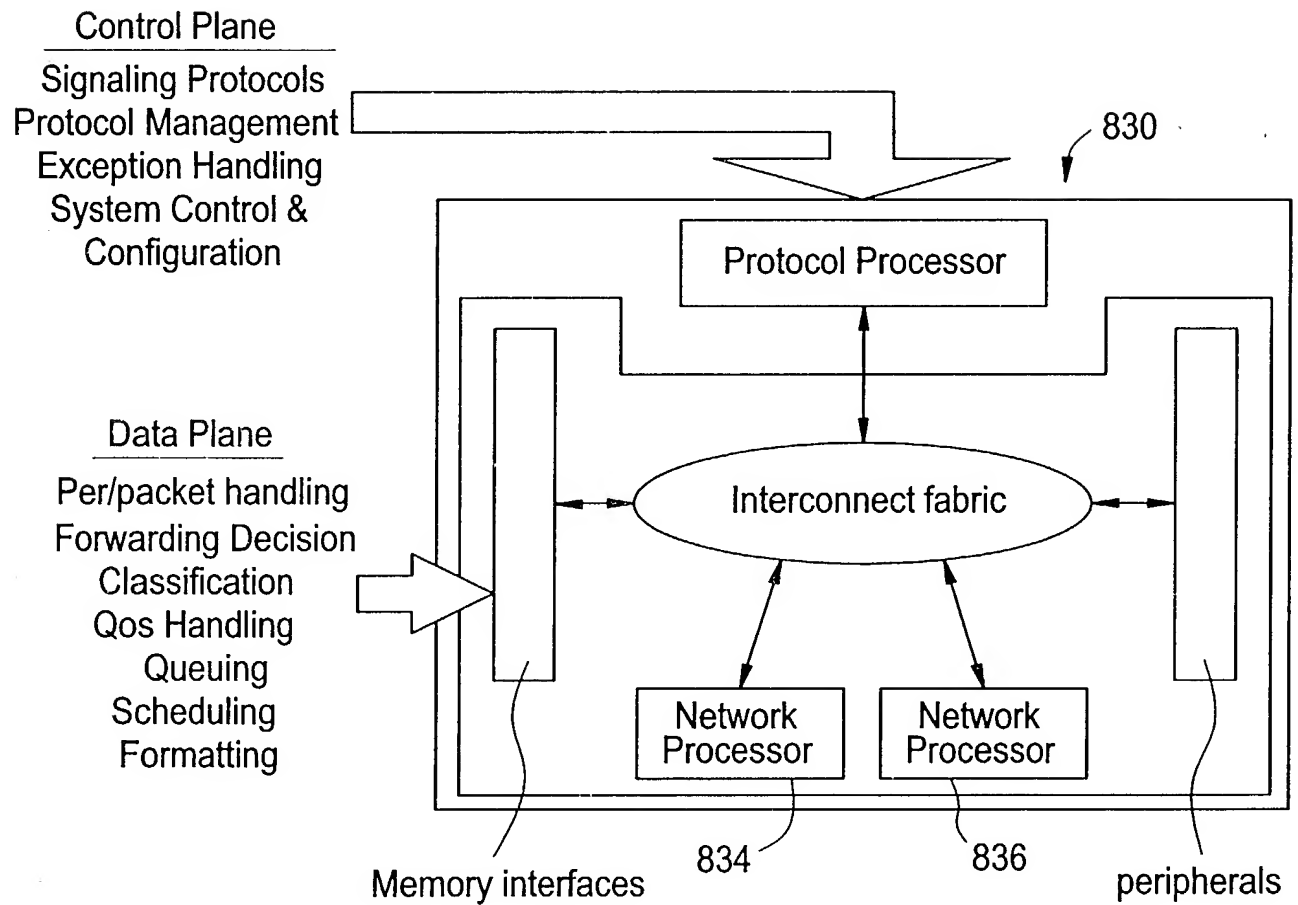
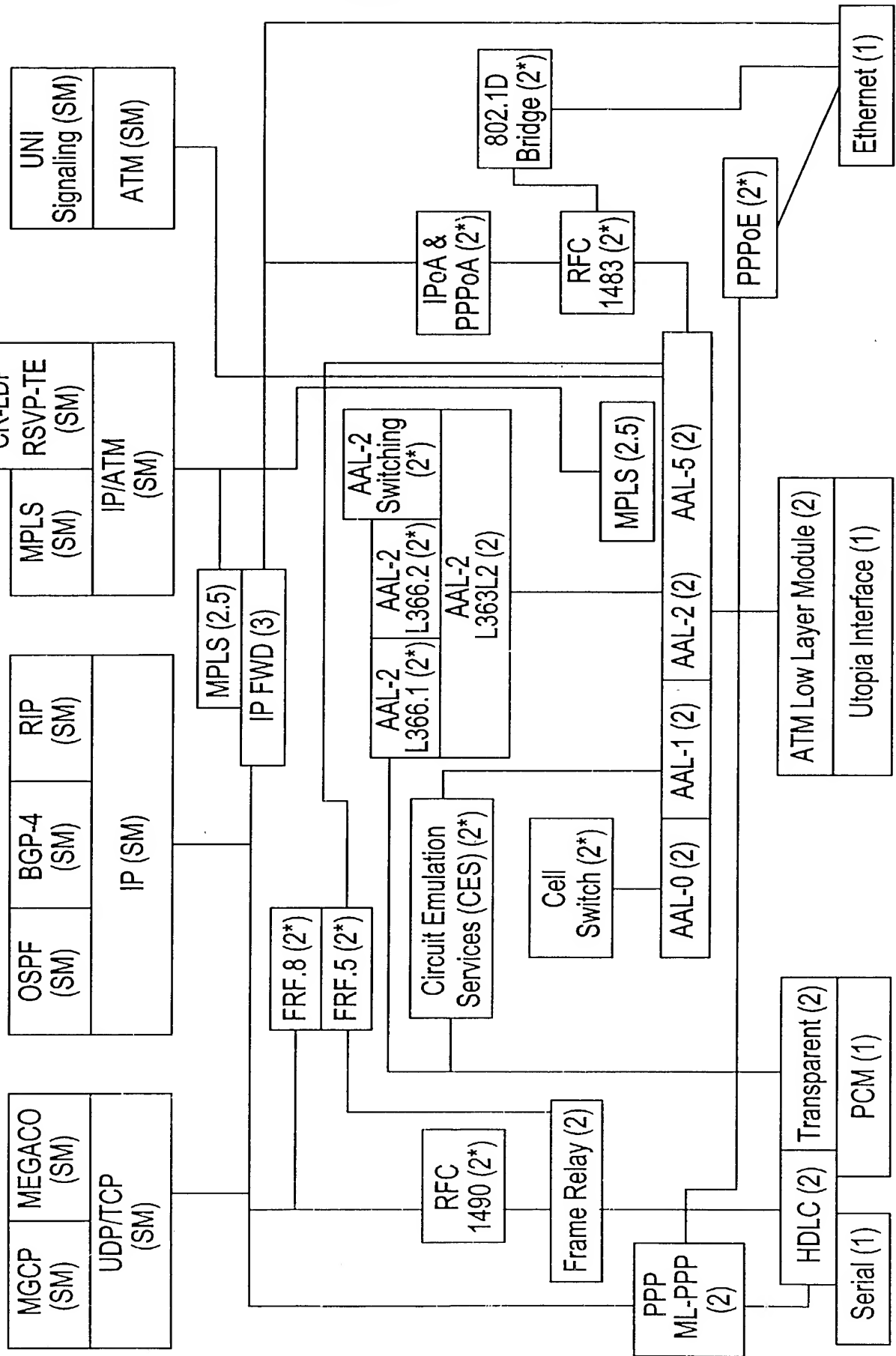
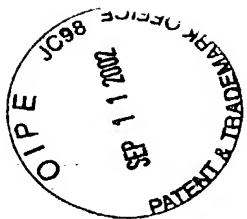


FIG. 71



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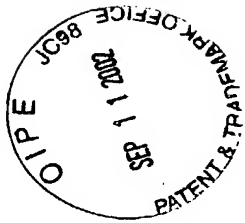
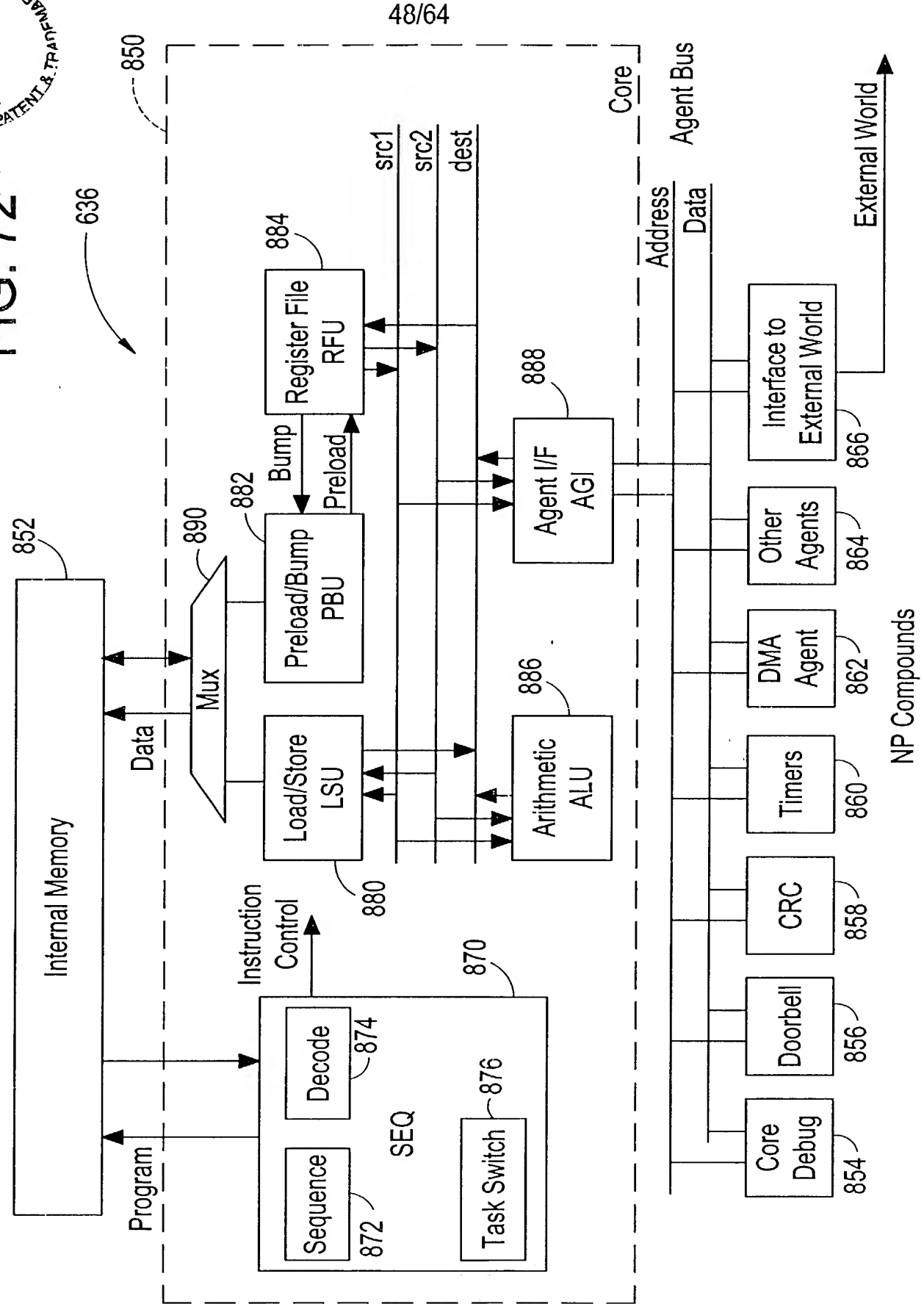


FIG. 72



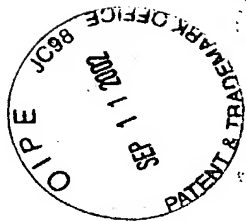
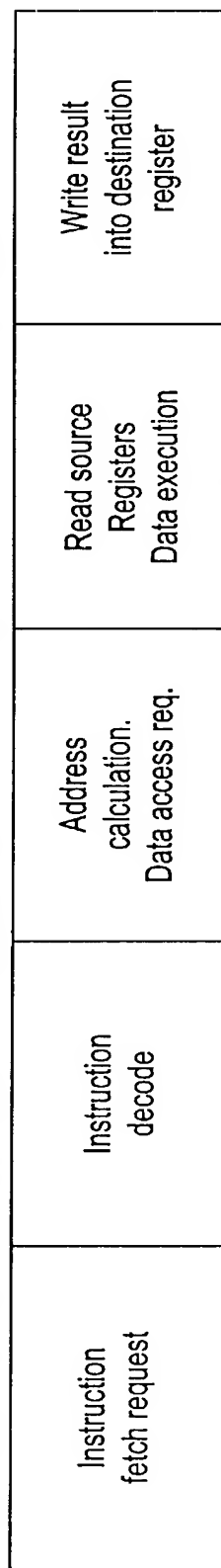
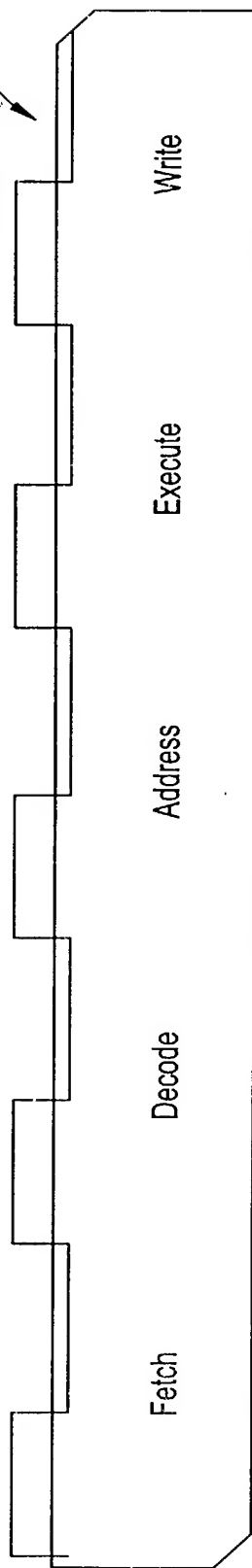
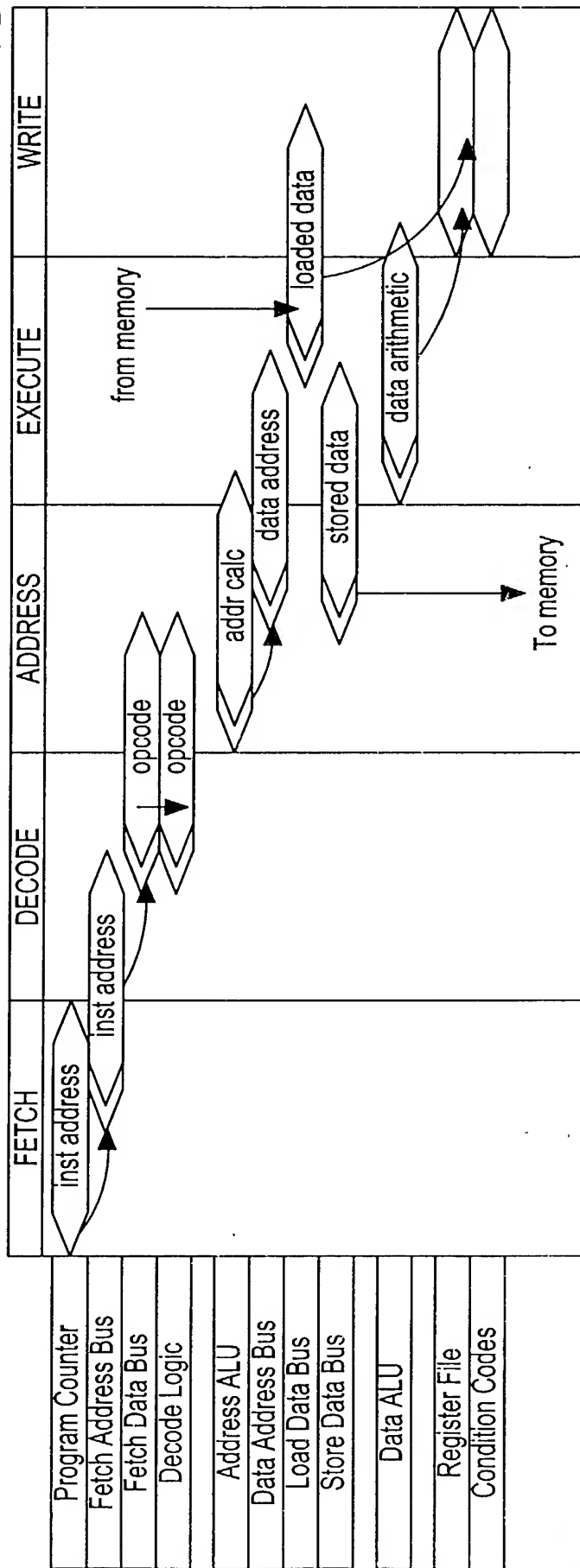


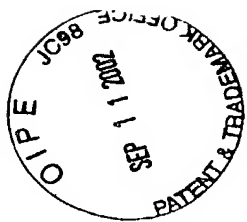
FIG. 73

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FIG. 74

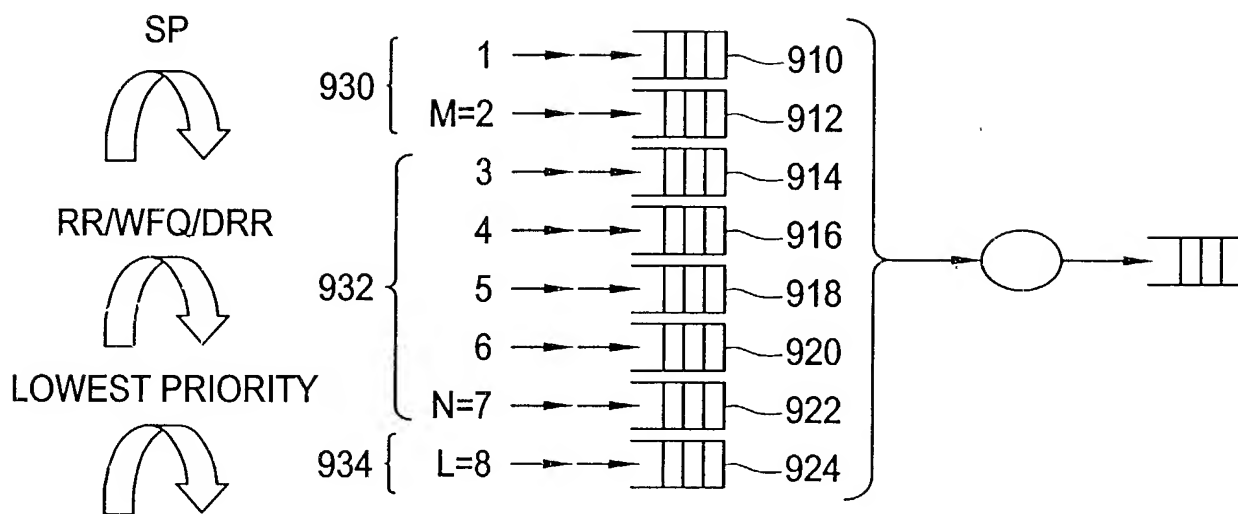
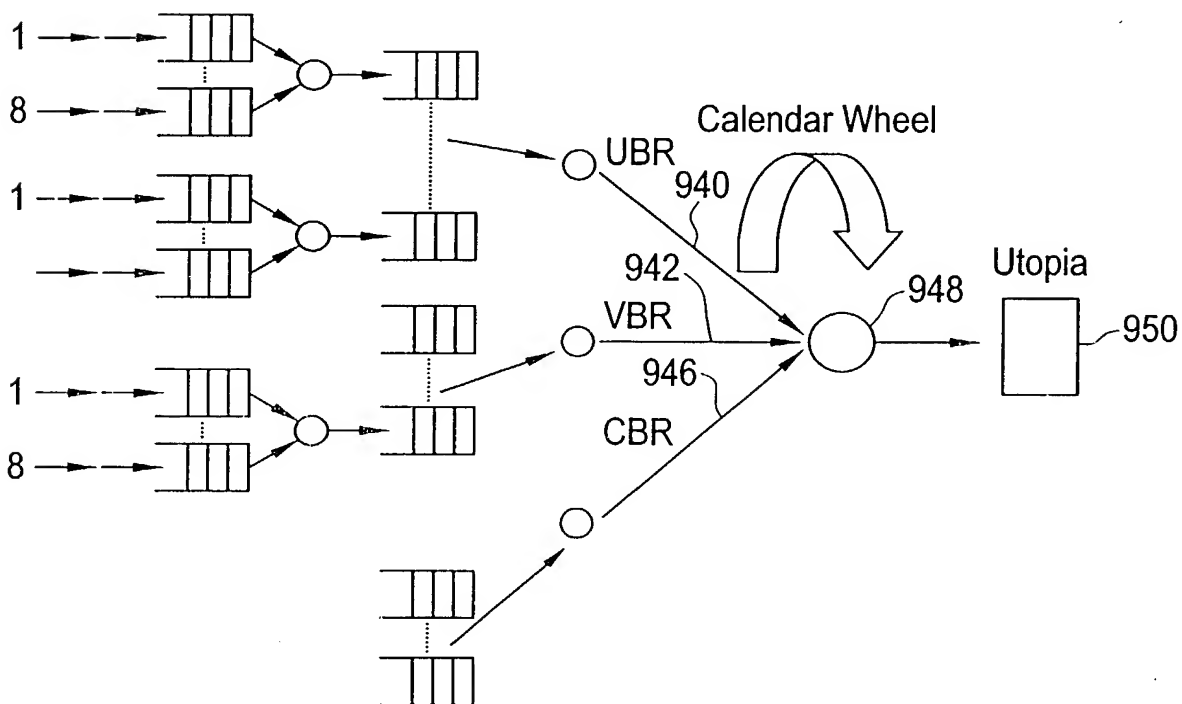


FIG. 75



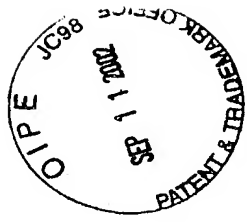


FIG. 76

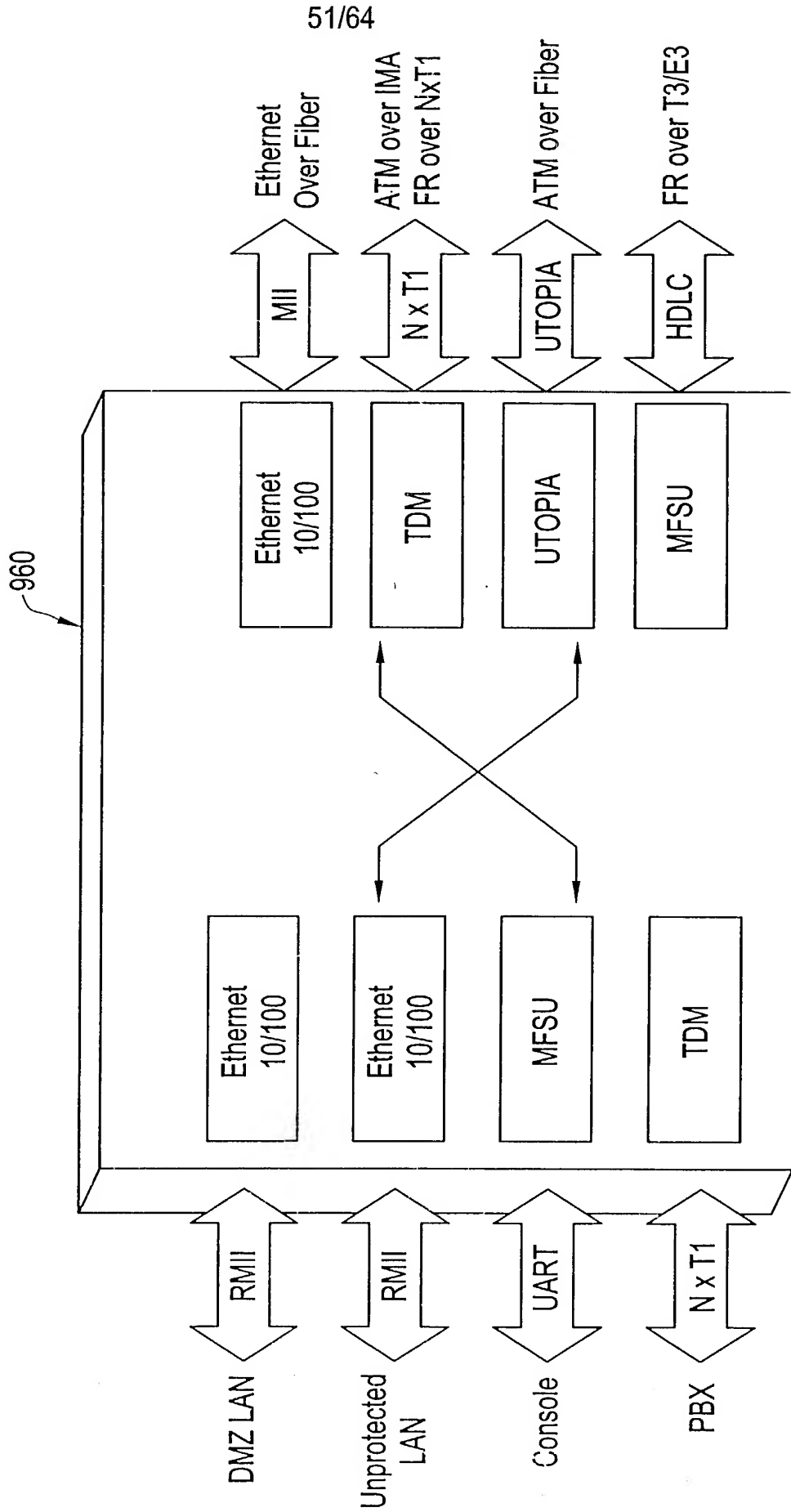
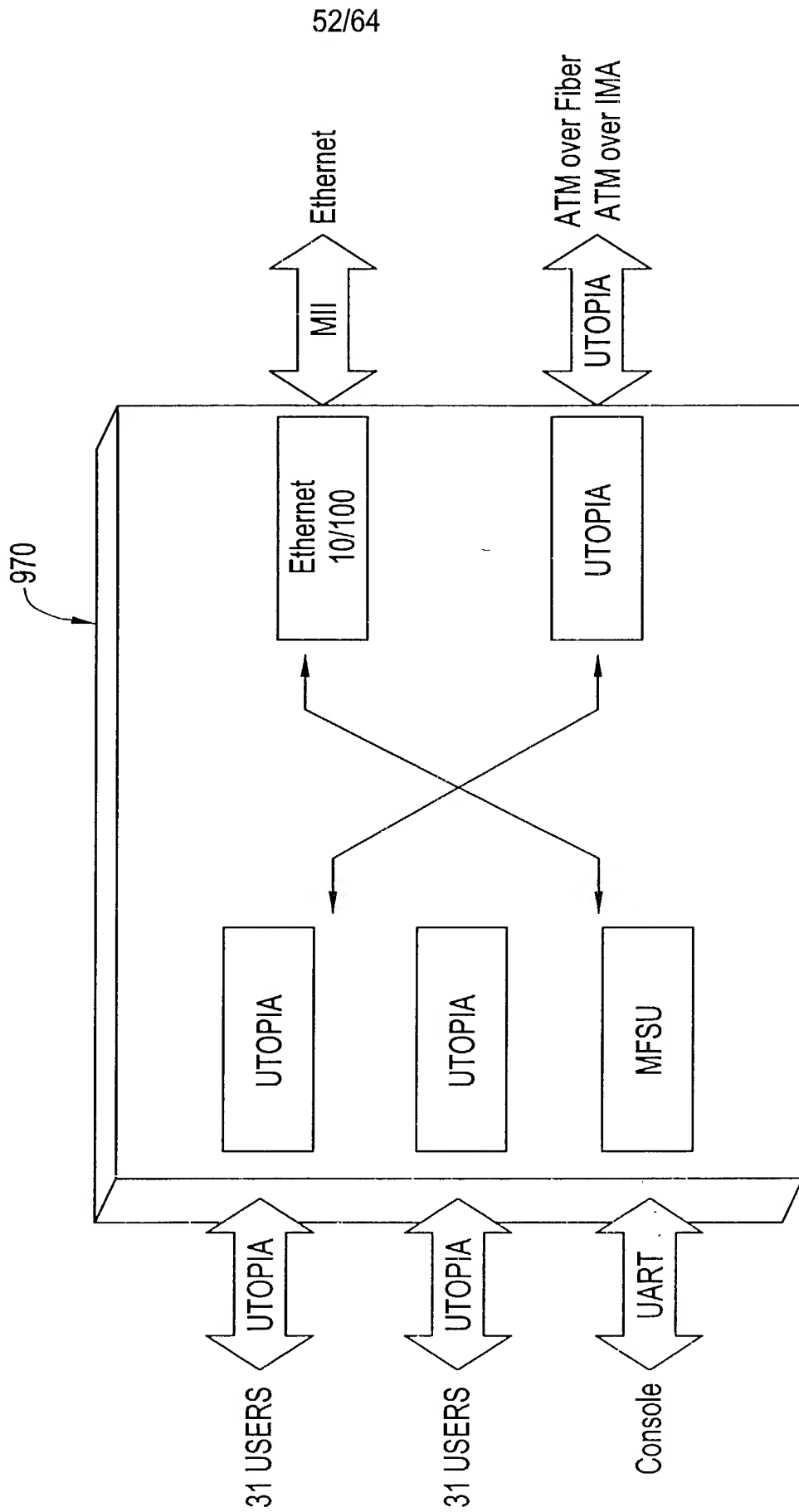


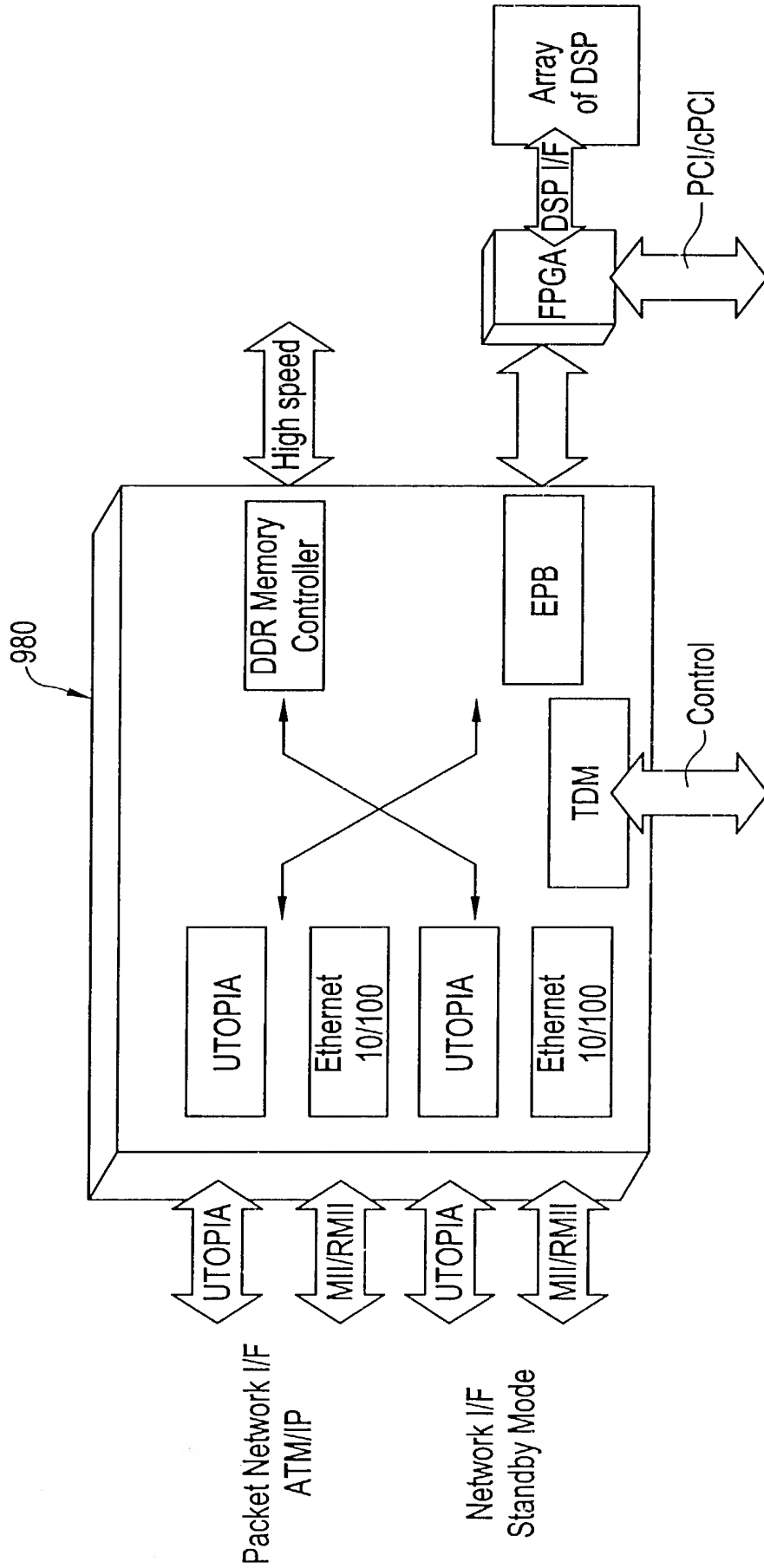
FIG. 77

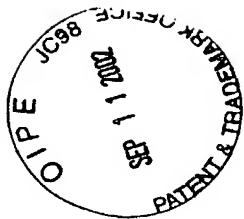


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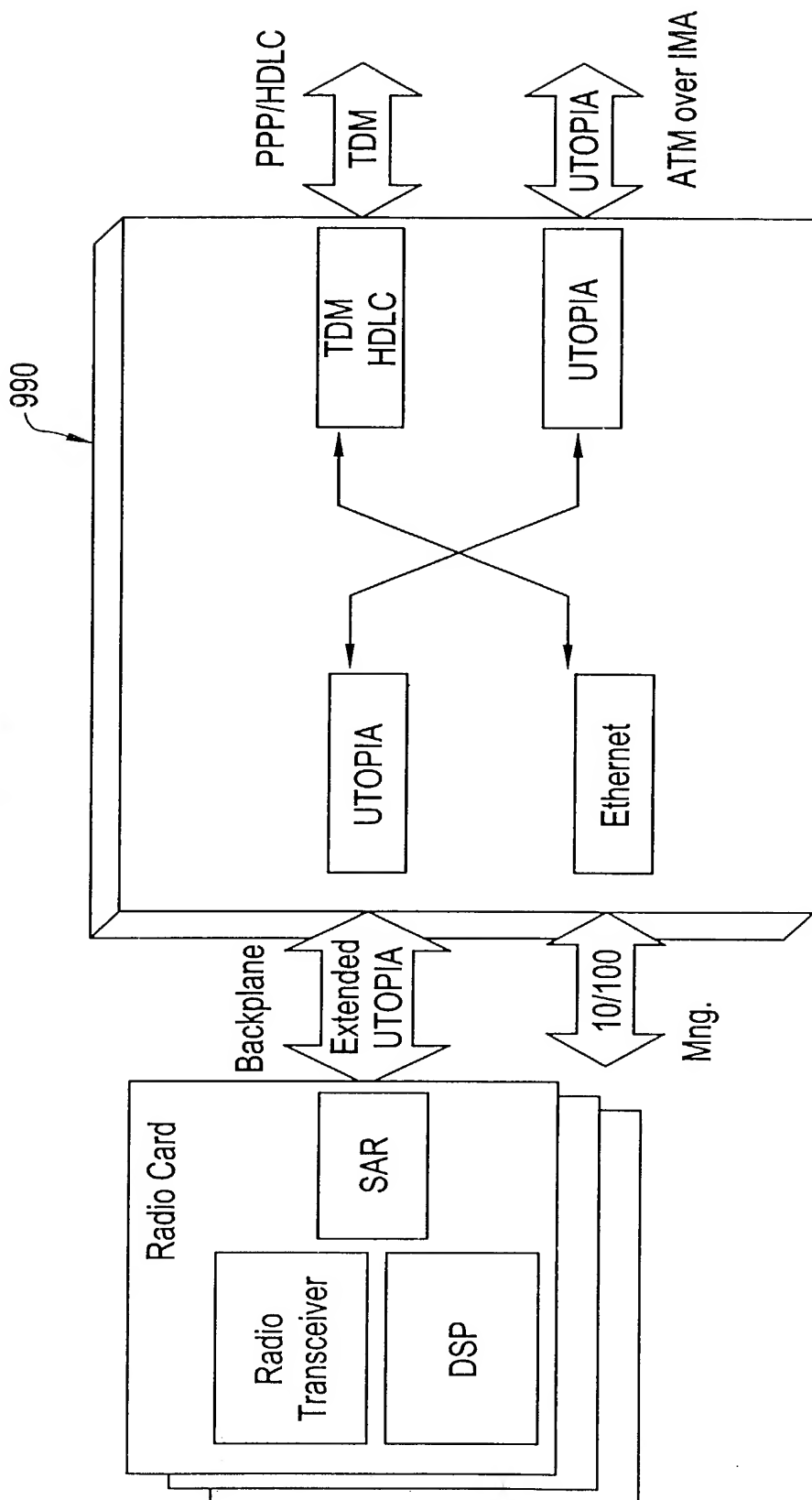
FIG. 78




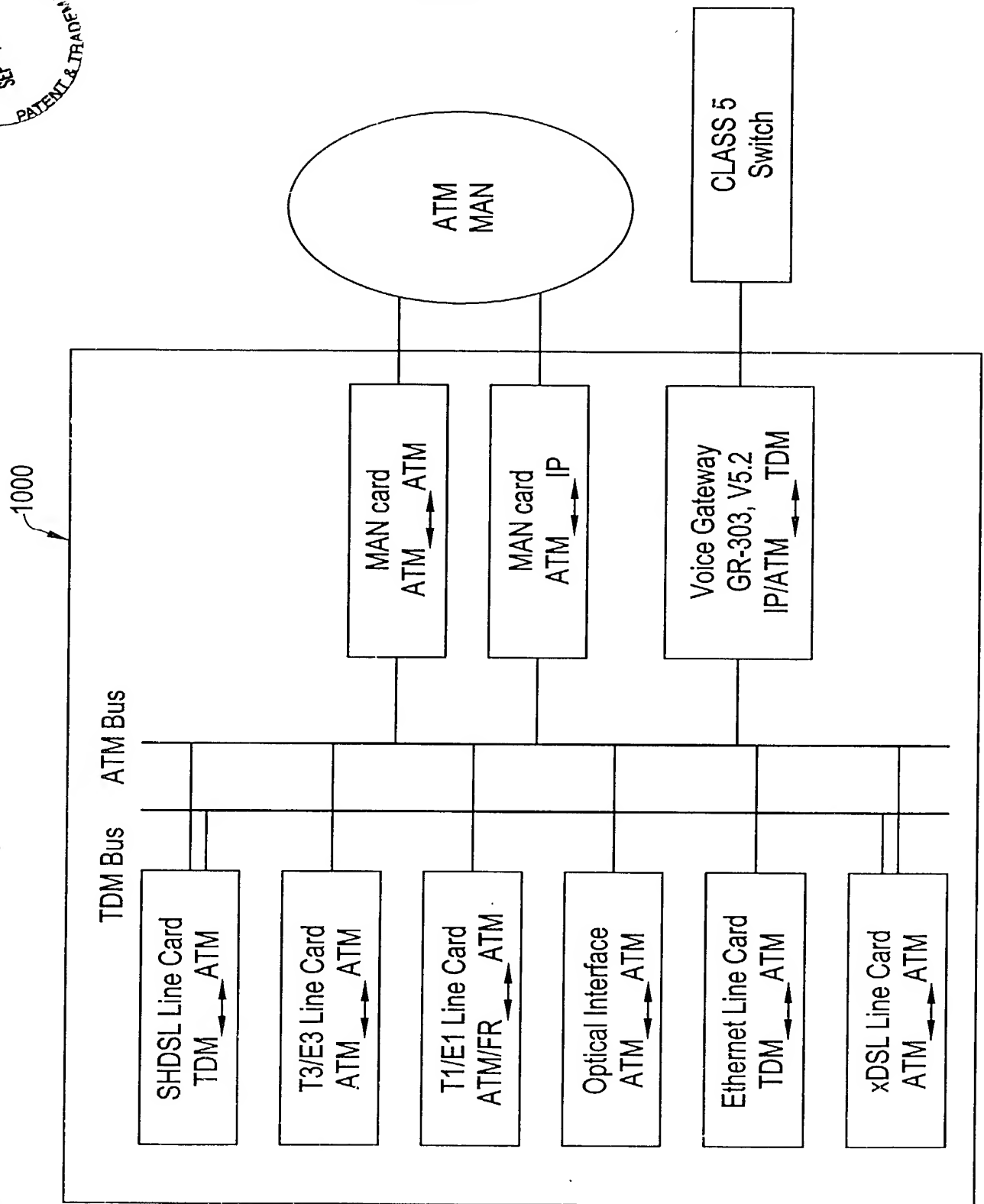


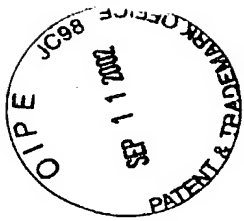
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FIG. 79









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FIG. 81

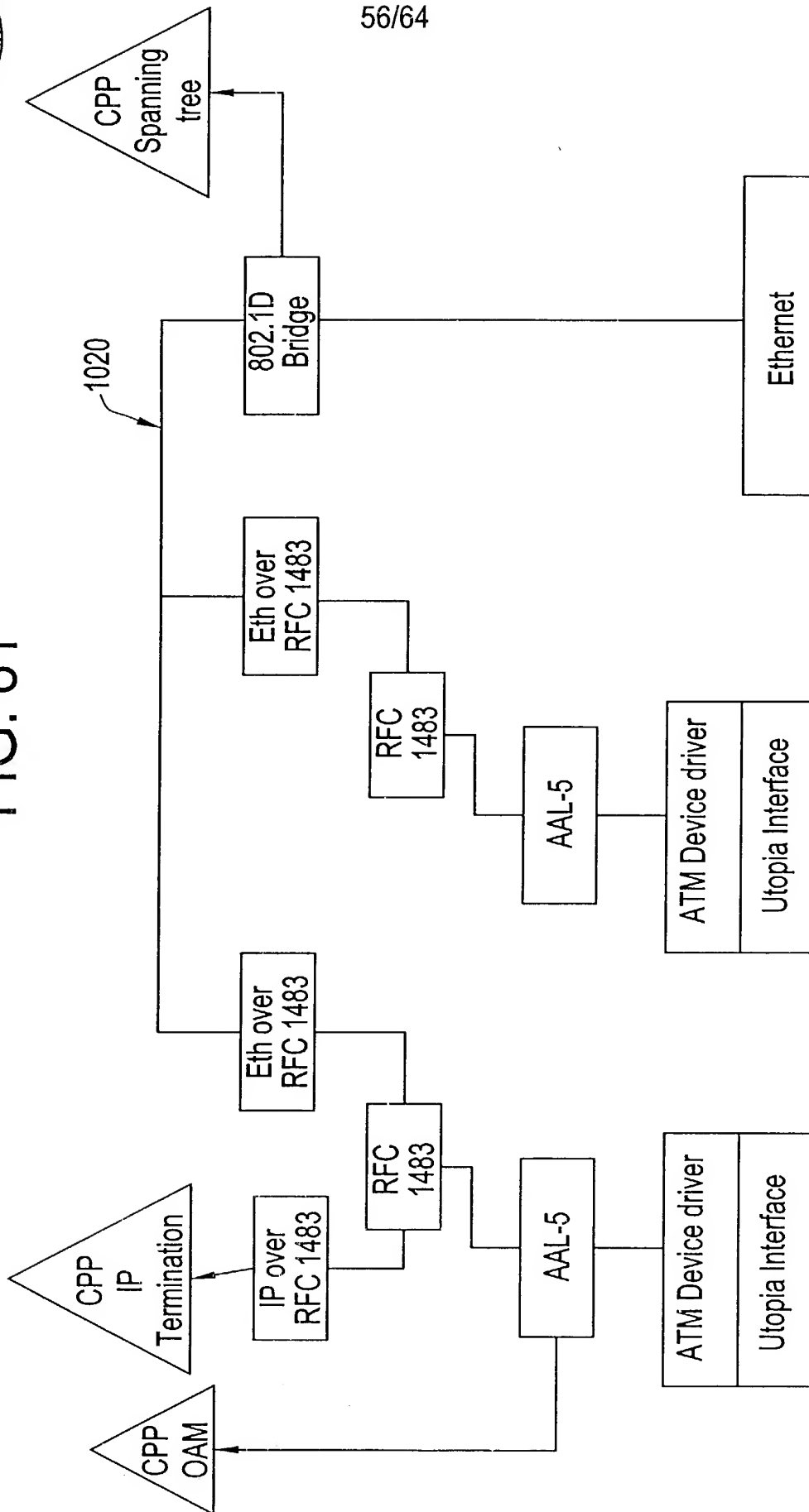
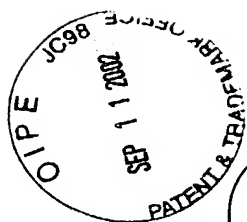
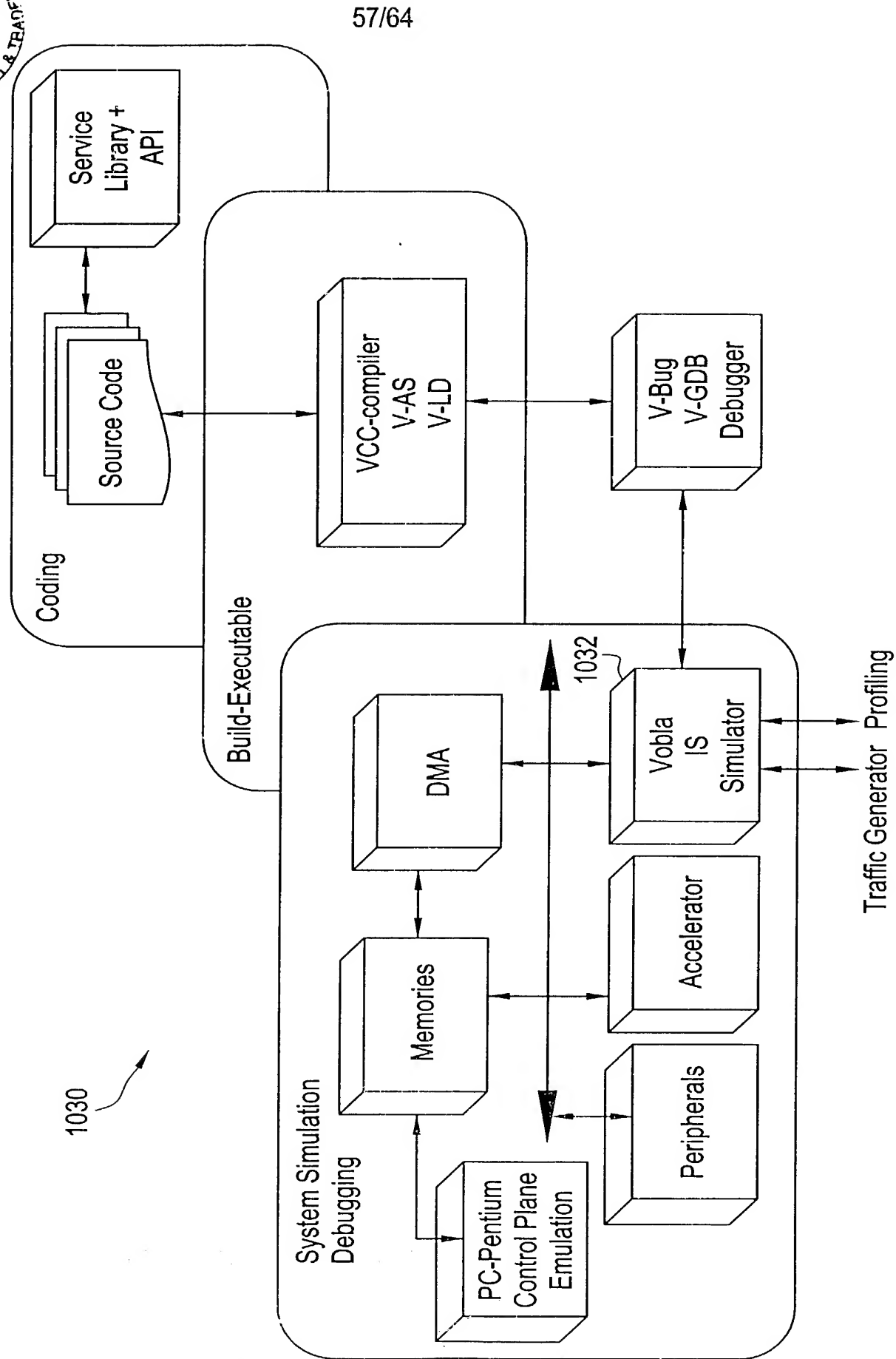


FIG. 82



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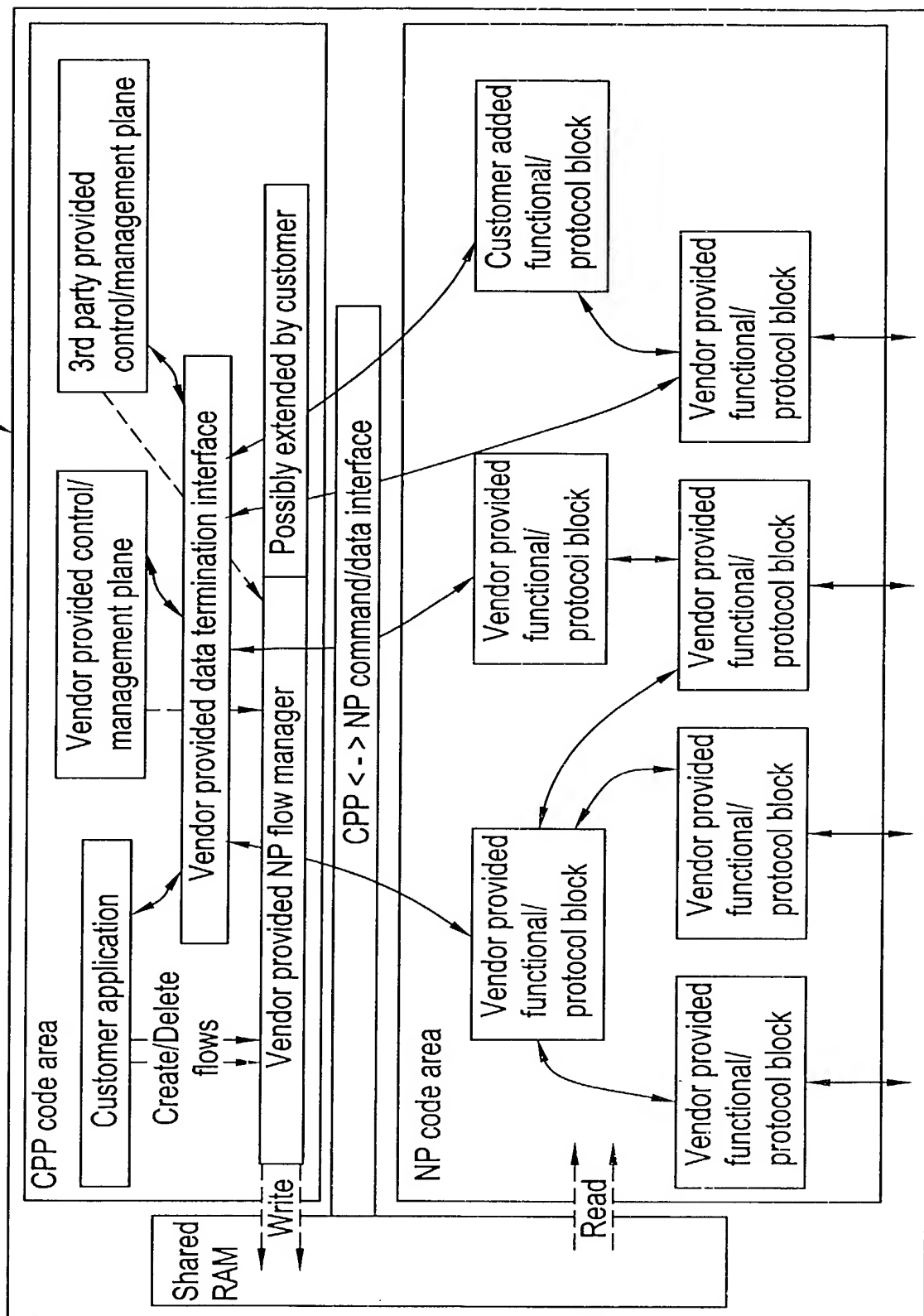
11 SEP 2002 11:11:11 AM



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FIG. 83

1040





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FIG. 84

1050

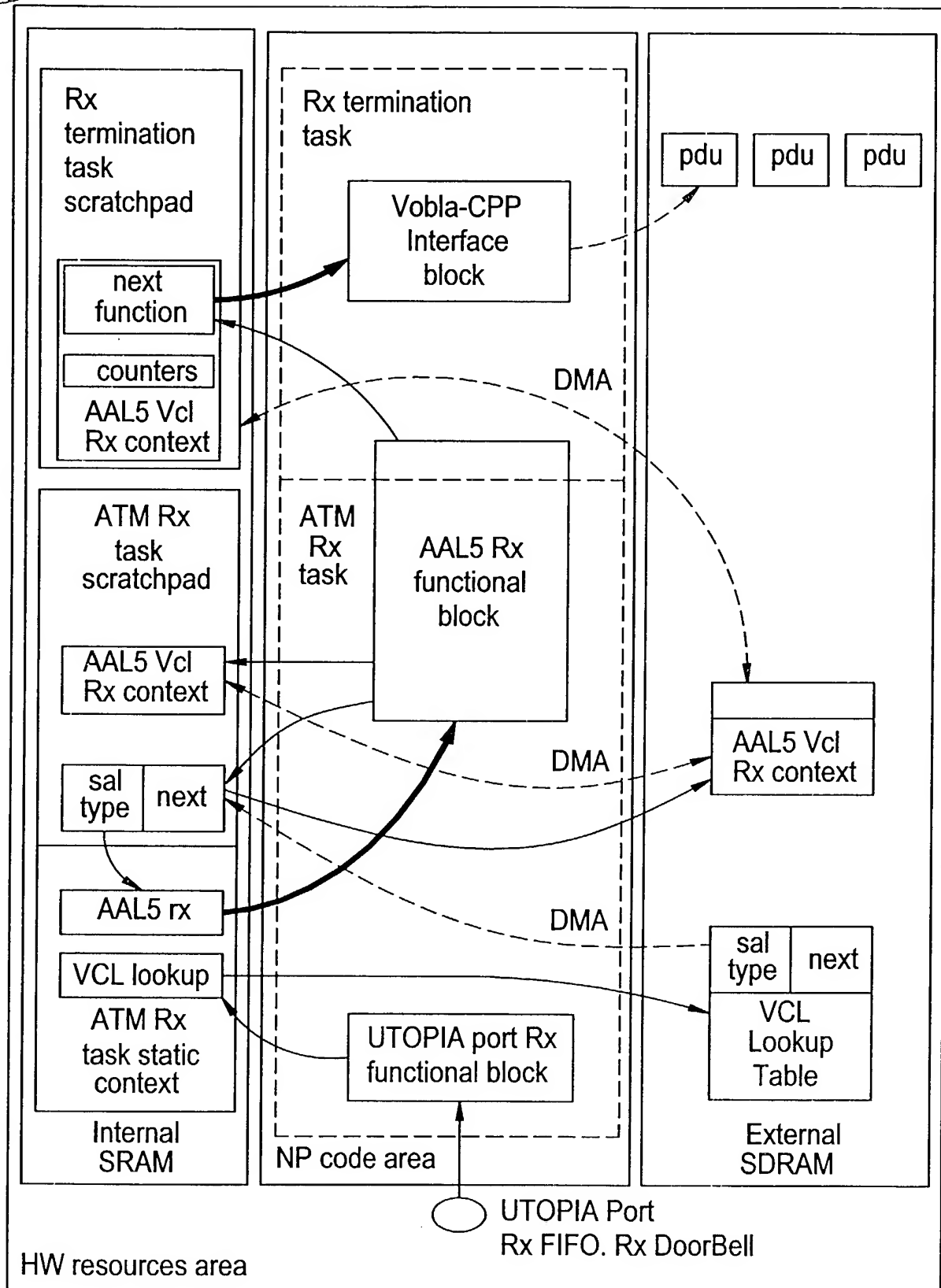


FIG. 85

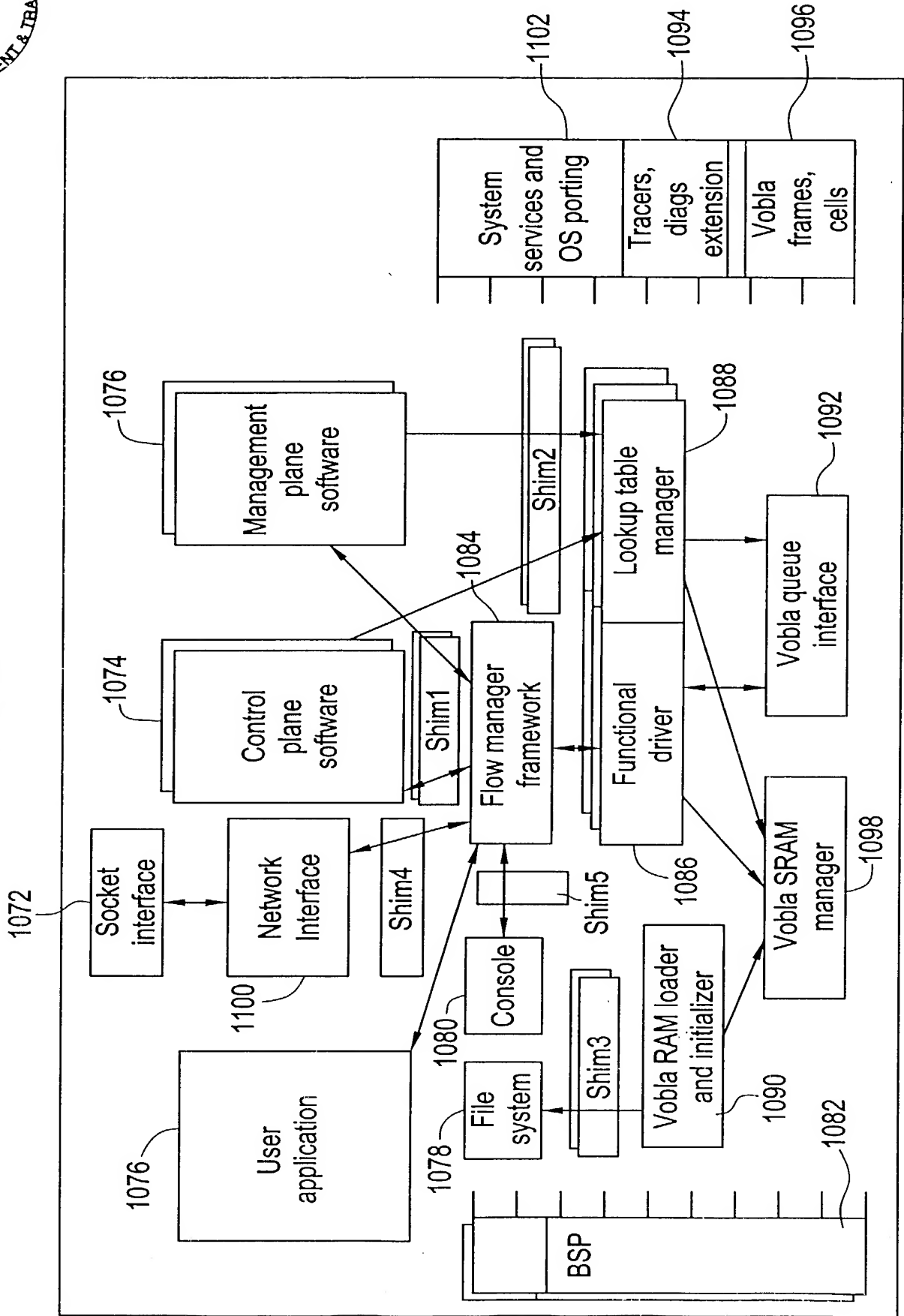
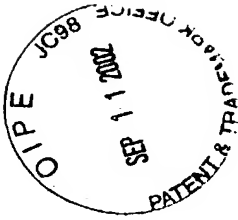
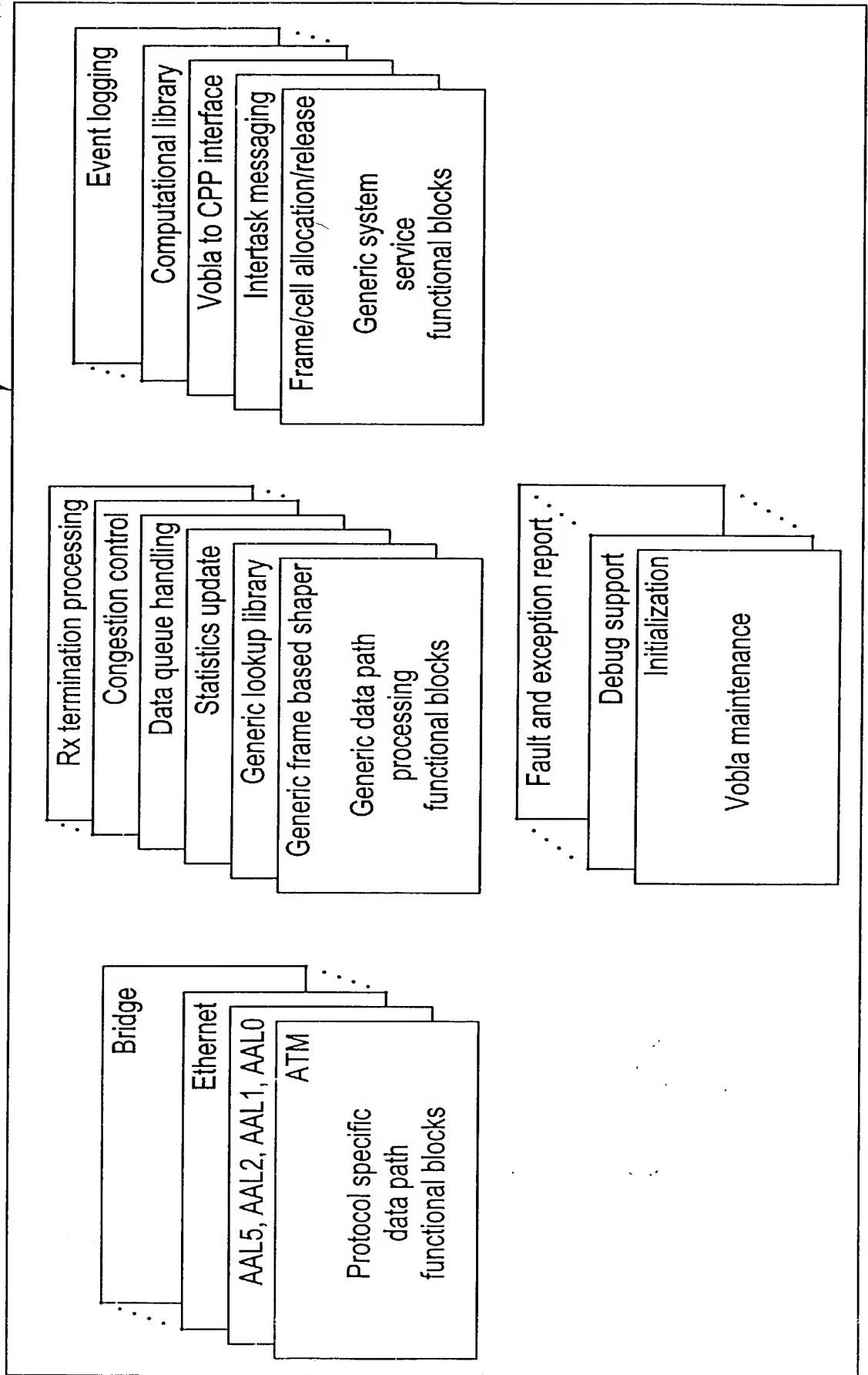


FIG. 86



1200

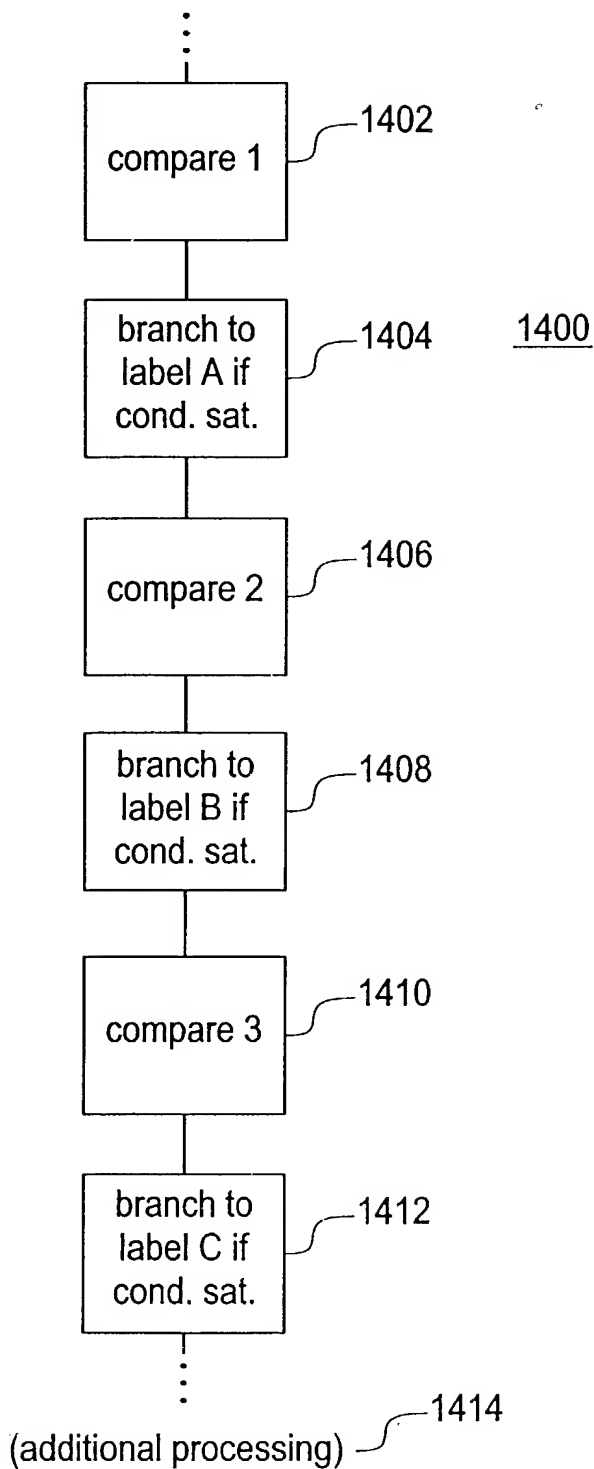


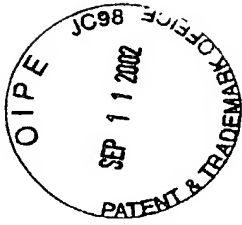


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FIG. 87

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FIG. 88

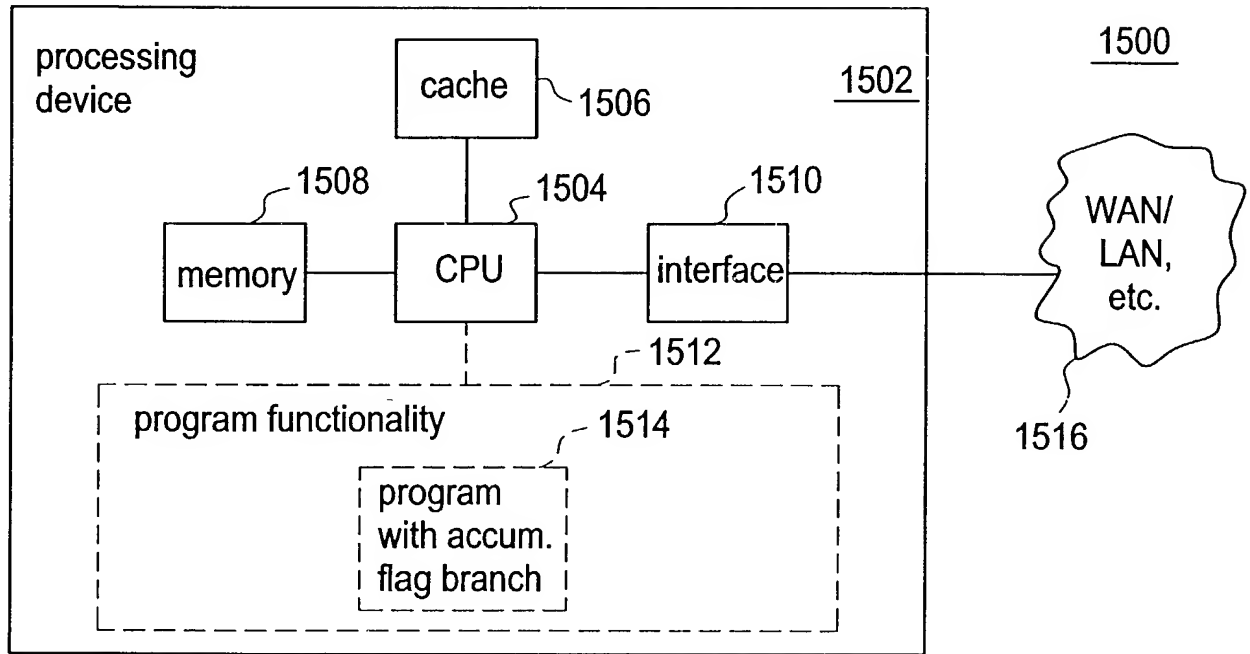
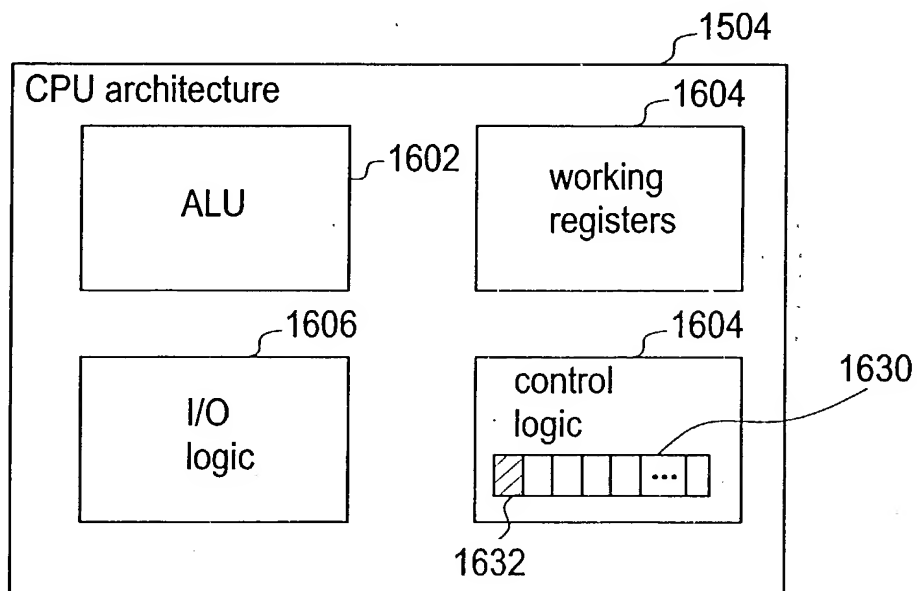
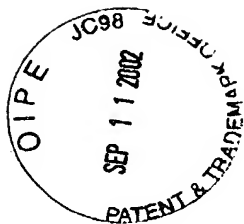


FIG. 89





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FIG. 90

